Problem 1

The scope captures below show part of the voltage waveform across a DBR diode (left) and the voltage across a 0.01 Ohm input sensing resistor (right). What are the conduction losses in the DBR diodes? If the output power is 300 W and the only losses in the DBR are those in the diodes, what is the DBR efficiency?

Now assume that the input voltage doubles. As a result of this input voltage increase, the output power is 1150 W, the peak input current doubles and the diodes conduction time increase 10 %. What are the new conduction losses and efficiency of the rectifier?

Note: For the left scope capture the vertical scale is 0.5V/division and for the right scope capture the vertical scale is 100 mV/division. In both cases the horizontal scale is 2 ms/division.
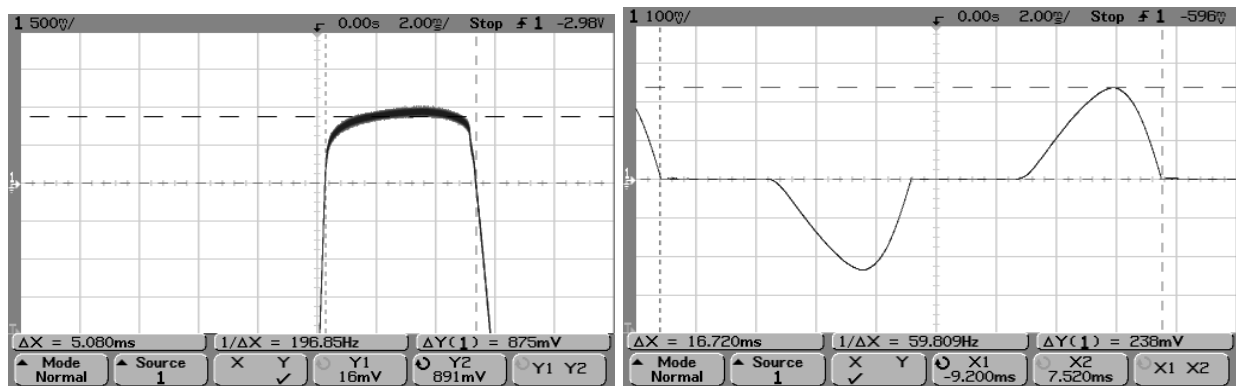
Problem 2

The light dimmer circuit was modified without including a capacitor (we assume that when the diac conducts it is still possible to generate a current pulse that triggers the triac). If the input voltage is 120 Vrms, and the diac breakover voltage is 35 V, what is value of R_P that yields a firing angle α of 30°? What is value of R_P that yields a firing angle α of 120°?
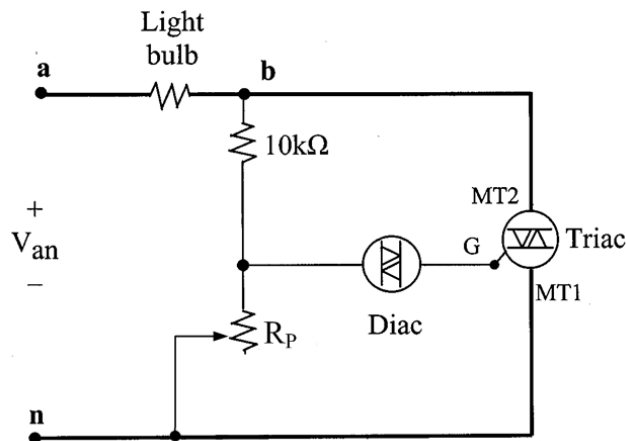
Please, show all your work on the test sheets. A correct answer without supporting work gets no credit. One sheet of notes is permitted. Write your name in all pages. Do not un-staple. Please **be neat**. Otherwise, you may end up loosing credit for correct answers if I cannot find or understand them. You have 50 minutes to complete the test.

Problem 1 (30 points)

Consider that you are measuring the harmonic content of a current signal with a 5 A, 100 mV shunt resistance, and your measurements with respect to a 1 mVrms reference are:

$\qquad$ V (0 Hz) = 40 dB
$\qquad$ V (60 Hz) = 20 dB
$\qquad$ V (180 Hz) = 17 dB
$\qquad$ V (300 Hz) = 12 dB

Sketch the FFT graph and calculate the current corresponding to each component. If this is the current measured at the output of a 12 Vdc voltage source, which is the power provided by this source?
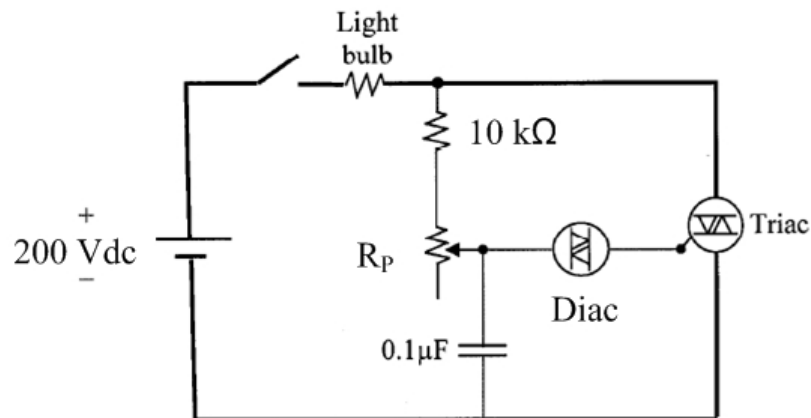
Problem 2 (25 points)

The thick trace in the figure below represents the output of a DBR with a 5 kW load. The DBR is connected a regular wall outlet in Europe with a 220 Vrms voltage and a 50 Hz frequency. What capacitance is needed on the DBR output in order to have a peak-to-peak voltage ripple of 5% of the peak voltage? Suppose that you travel to the US where the line frequency is 60 Hz and the voltage is 120 Vrms. Does the voltage ripple change? If so, what's its new value?
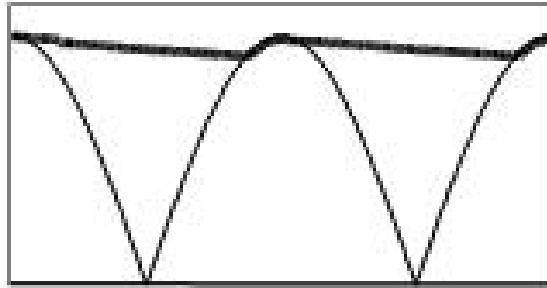
Problem 3 (30 points)

The light dimmer circuit was modified by including a switch and replacing your ac source by a dc source. When the switch closes at t = 0, the capacitor is fully discharged. When the capacitor voltage reaches 50 V, the light bulb turns on and stays on. What is the value of R_p that makes the light bulb to go on at t = 5 miliseconds. Hint: you may ignore the light bulb resistance. The capacitor charges according to $v_C(t) = V_F\left(1 - e^{-\frac{t}{RC}}\right)$ where V_F is the fully charged voltage.

Light
bulb

10 kΩ

+
200 Vdc

R_P

Triac

Diac

0.1μF

Problem 4 (5 points each)

Please, select the correct answer for the following questions. **Provide a justification for your answers.**

4.1) Suppose you are on the equator and you want to maximize the solar radiation received on a solar panel on December 21. Then you will orient the panel in the following way:
   a) Azimuth 180º, Tilt 23.45º
   b) Azimuth 0º, Tilt 23.45º
   c) Azimuth 180º, Tilt 0º
   d) Azimuth 0º, Tilt 0º
   e) None of the above
   Remember that azimuth 0º = North

4.2) Consider a gate drive circuit for a MOSFET. The saw-tooth signal at the PWM chip has varies between ground and 5 V. In order to obtain a constant duty cycle of 0.4 the amplitude of the control reference voltage is
   a) 0V
   b) 1 V
   c) 2 V
   d) 3 V
   e) 4 V
   f) 5 V
   g) None of the above

4.3) For each of the following statements choose the correct answer
   a) It is OK to connect an inductor in series with a switch
               TRUE   FALSE

   b) It is OK to connect a capacitor in series with a switch.
               TRUE   FALSE

   c) In steady state the average current though an inductor is zero.
               TRUE   FALSE

   d) In steady state the average voltage though an inductor is zero.
               TRUE   FALSE