Lab 2 grading sheet

Students name 1) Last_____ First_____ EID_____

*Use same spelling as listed on Blackboard*

Students name 2) Last_____ First_____ EID_____

Circle instructor:

| | |
|---|---|
| Valvano | TTh5 |
| Telang | MWF2 |
| Yerraballi | TTh3:30 or MW3 |
| Gerstlauer | TTh2 |

1. Deliverables 20%: [ ]

This sheet

*Combine the following components into one file (pdf, doc, or docx) and upload it to Blackboard before your checkout time. Have this file open on the computer during demonstration. In lieu of uploading the one file you can print these items and staple them together.*

A screenshot, like Figure 2.1, (microcomputer, logic analyzer, and I/O windows)

Assembly source code (RTF file) of your final program

Measurement of how much 9S12 time is simulated in 10 seconds of actual time.

2. Performance 40%: [ ]

Does it handle correctly all situations as specified?

How pretty is the software?

| | 1) | 2) |
|---|---|---|
| 3. Demonstration 40%: | | |

Can you explain to the TA how your software works?

You will show the TA your program operation on the TExaS simulator. Be prepared to explain how the delay function works. How would it be different if it were 100 ms instead of 1 ms? The TA will pick an instruction from your code and ask you which addressing mode it uses. Execute the program step-by-step and run to cursor. What is a Reset Vector and why do you need it? What if you change the **`org $4000`** to **`org $3800`**? What does **`org`** do? What does **`fdb`** do?

| | 1) | 2) |
|---|---|---|
| Total: | | |