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First: $\qquad$ Last: $\qquad$
October 7, 2011, 2:00pm-2:50pm. This is a closed book exam. You have 50 minutes, so please allocate your time accordingly. Please read the entire quiz before starting.

```
#define GPIO_PORTG_DATA_R (*((volatile unsigned long *)0x400263FC))
// all 8 bits of Port G
#define GPIO_PORTG_DIR_R (*((volatile unsigned long *)0x40062400))
// direction register for Port G
#define GPIO_PORTG_AFSEL_R (*((volatile unsigned long *)0x40026420))
// alternate function for Port G (0 for normal I/0, 1 for alternate I/0)
#define GPIO_PORTG_DEN_R (*((volatile unsigned long *)0x4002651C))
// data enable register for Port G
#define GPIO_PORTG0 (*((volatile unsigned long *)0x40026004))
// GPIO_PORTG0 is just bit 0
#define GPIO_PORTG1 (*((volatile unsigned long *)0x40026010))
// GPIO_PORTG1 is just bit 1
#define SYSCTL_RCGC2_R (*((volatile unsigned long *)0x400FE108))
// set bit 6 to turn on Port G clock
#define NVIC_ST_CURRENT_R (*((volatile unsigned long *)0xE000E018))
// 24-bit down counter running at the bus clock
```

| $+>0$ | $\begin{aligned} & \mathrm{V}_{\mathrm{OL}}=0.5 \mathrm{~V} \\ & \mathrm{I}_{\mathrm{OL}}=40 \mathrm{~mA} \\ & \mathrm{~V}_{\mathrm{IL}}=0.7 \mathrm{~V} \\ & \mathrm{I}_{\mathrm{IL}}=1.6 \mathrm{~mA} \end{aligned}$ |
| :---: | :---: |
|  | $\begin{aligned} & \mathrm{V}_{\mathrm{ce}}=0.3 \mathrm{~V} \\ & \mathrm{~V}_{\mathrm{be}}=0.6 \mathrm{~V} \\ & \mathrm{~h}_{\mathrm{fe}}=100 \\ & \mathrm{I}_{\mathrm{ce}}=500 \mathrm{~mA} \text { max } \end{aligned}$ |

LM3Sxxx parameters
$\mathrm{I}_{\mathrm{OL}}=2,4$ or 8 mA ,
$\mathrm{I}_{\mathrm{OH}}=2,4$ or 8 mA ,
$\mathrm{I}_{\mathrm{IL}}=2 \mu \mathrm{~A}$,
$\mathrm{I}_{\mathrm{IH}}=2 \mu \mathrm{~A}$,
$\mathrm{V}_{\mathrm{OL}}=0.4 \mathrm{~V}$,
$\mathrm{V}_{\mathrm{OH}}=2.4 \mathrm{~V}$,
$\mathrm{V}_{\mathrm{IL}}=1.3 \mathrm{~V}$,
$\mathrm{V}_{\mathrm{IH}}=2.0 \mathrm{~V}$

Assuming the $\mathrm{V}_{\mathrm{CC}}$ of the XBee is $3.3 \mathrm{~V}, 0.35^{*} \mathrm{~V}_{\mathrm{CC}}=1.155 \mathrm{~V}, 0.7^{*} \mathrm{~V}_{\mathrm{CC}}=2.31 \mathrm{~V}, \mathrm{~V}_{\mathrm{CC}}-0.5 \mathrm{~V}=2.8 \mathrm{~V}$
Table 1-03. DC Characteristics (VCC $=2.8$ - 3.4 VDC )

| Symbol | Characteristic | Condition | Min | Typical |  | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\mathrm{IL}}$ | Input Low Voltage | All Digital Inputs | - | - |  | 0.35 * VCC | V |
| $\mathrm{V}_{\mathrm{IH}}$ | Input High Voltage | All Digital Inputs | 0.7 * VCC | - |  | - | V |
| $\mathrm{V}_{\mathrm{OL}}$ | Output Low Voltage | $\mathrm{I}_{\mathrm{OL}}=2 \mathrm{~mA}, \mathrm{VCC}>=2.7 \mathrm{~V}$ | - | - |  | 0.5 | V |
| $\mathrm{V}_{\mathrm{OH}}$ | Output High Voltage | $\mathrm{I}_{\mathrm{OH}}=-2 \mathrm{~mA}, \mathrm{VCC}>=2.7 \mathrm{~V}$ | VCC - 0.5 | - |  | - | V |
| $\\|_{\mathbb{N}}$ | Input Leakage Current | $\mathrm{V}_{\mathbb{N}}=\mathrm{VCC}$ or GND, all inputs, per pin | - | 0.025 |  | 1 | $\mu \mathrm{A}$ |
| $\mathrm{II}_{\mathrm{OZ}}$ | High Impedance Leakage Current | $\mathrm{V}_{\mathbb{N}}=$ VCC or GND, all //O High-Z, per pin | - | 0.025 |  | 1 | $\mu \mathrm{A}$ |
| TX | Transmit Current | $\mathrm{VCC}=3.3 \mathrm{~V}$ | - | $\begin{gathered} 45 \\ \text { (XBee) } \end{gathered}$ | $\begin{gathered} 215 \\ (\mathrm{PRO}) \end{gathered}$ | - | mA |
| RX | Receive Current | $\mathrm{VCC}=3.3 \mathrm{~V}$ | - | $\begin{gathered} 50 \\ \text { (XBee) } \end{gathered}$ | $\begin{gathered} 55 \\ (\mathrm{PRO}) \end{gathered}$ | - | mA |
| PWR-DWN | Power-down Current | SM parameter $=1$ | - | < 10 |  | - | $\mu \mathrm{A}$ |

(15) Question 1. Assume SysTick is running with a reload value of 0x00FFFFFF. Write a minimally intrusive debugging instrument that records the 24-bit SysTick timer (NVIC_ST_CURRENT_R) at the time the instrument is called. For the first 256 measurements, you simply record the time. After 256 recordings, you will discard the oldest when a new measurement is obtained. Your solution will store the measurements in any order. Show the variables, initialization, and the instrument.
(20) Question 2. In this question you must use semaphores. There is an ISR and a main program, which are shown below. The interrupt has priority equal to 4 . The goal of the problem is force Fun0 and Fun1 to be executed alternately (i.e., Fun0, Fun1, Fun0, Fun1, Fun0, Fun1, Fun0, Fun1,...) with Fun0 going first. You should add code, but may not delete any existing code. You may not add additional calls to Fun0 or Fun1. You are not allowed to arm, disarm, enable, disable, or change the timer interrupt rate. I.e., you cannot access any timer or NVIC registers. You may not add any backward jumps, but are allowed ifthen statements.

## void Timer0A_Handler(void)\{

```
    Fun0();
```

TIMER0_ICR_R $=$ TIMER_ICR_TATOCINT; // acknowledge timer0A timeout
$\}$ void main(void) $\{$
while(1) \{
Fun1();
\}
\}
(10) Question 3. In Lab 10 we will be interfacing an XBee wireless module to the LM3S1968.

Part a) Can we connect the LM3S1968 input to an XBee output? If yes, give all inequalities with both symbolic and numerical values to prove it works. If no, give at least one inequality both symbolic and numerical values that is not satisfied.

Part b) Can we connect the LM3S1968 output to an XBee input? If yes, give all inequalities with both symbolic and numerical values to prove it works. If no, give at least one inequality both symbolic and numerical values that is not satisfied.
(15) Question 4. The following multithreaded system uses Port G as a debugging profile. PG0 goes high during ISR0 and PG1 goes high during ISR1. One interrupt has higher priority than the other. Stuff0 and Stuff1 are unrelated. Is there a critical section? If yes, explain how to remove his particular critical section. If no, justify your answer in 16 words or less.

```
void ISR0(void){
    GPIO_PORTG_DATA_R ^= 0x01;
    Stuff0();
    }
        void ISR1(void) {
    GPIO_PORTG_DATA_R ^= 0x02;
    Stuff1();
}
The assembly code for ISR0 is
ISR0 PUSH \{lr\}
LDR r0,[pc,\#180] ; R0 points to Port G
LDR \(\quad\) 0, [r0,\#0x00] ; R0 contains the value from Port G
EOR r0,r0,\#0x01 ; toggle bit 0
LDR r1,[pc,\#152] ; R1 points to Port G
STR r0,[r1,\#0x3FC] ; write new value to Port G
BL.W Stuff0 ; call function
POP \{pc\} ; return from interrupt
```

(10) Question 5. You can convert any Mealy FSM to a Moore FSM and convert any Moore FSM to a Mealy FSM. Typically, Moore machines have a simpler controller, while Mealy machines have fewer states. One minor difference is Mealy machines get the first output after the first input, while a Moore machine has a first output before it senses the first input. In this Mealy machine the state transition arrows are input/output. You may assume the initial state is Hungry and the initial input is 0 . The execution order for this Mealy is input, output (depends on input and state), then next (depends on input and state).


Redraw this FSM graph in Moore format. The execution order for the Moore machine is output (depends on state), input, then next (depends on input and state). Just draw the Moore graph (no hardware or software required).
(15) Question 6. The objective is to implement a function to calculate the surface area of a cylinder using fixed-point calculations. $\mathbf{r}$ is radius of the cylinder, which can vary from 0 to 1 cm . The radius is stored as a fixed point number with resolution 0.001 cm . The software variable containing the integer part of the radius is $\mathbf{n}$, which can vary from 0 to 1000 . The height of the cylinder is 1 cm . The surface area is approximated by (I greatly simplified $2 \pi$, so the math would be easier without a calculator)

$$
\mathbf{s}=6.2 *\left(\mathbf{r}^{2}+\mathbf{r}\right)
$$

The surface area can range from 0 to $12.400 \mathrm{~cm}^{2}\left(6.2^{*}\left(1^{2}+1\right)\right)$. The surface area is stored as a fixed point number with resolution $0.001 \mathrm{~cm}^{2}$. The software variable containing the integer part of the surface area is $\mathbf{m}$, which can vary from 0 to 12400 . Write $\mathbf{C}$ code to implement (no floating point allowed) the conversion from $\mathbf{n}$ to $\mathbf{m}$. This table shows some typical results

| $\mathbf{r}(\mathbf{c m})$ | $\mathbf{n}$ | $\mathbf{s}\left(\mathbf{c m}^{\mathbf{2}}\right)$ | $\mathbf{m}$ |
| ---: | ---: | ---: | ---: |
| 0.000 | 0 | 0.000 | 0 |
| 0.001 | 1 | 0.006 | 6 |
| 0.010 | 10 | 0.063 | 63 |
| 0.100 | 100 | 0.682 | 682 |
| 1.000 | 1000 | 12.400 | 12400 |

## unsigned long n,m;

You are allowed to define additional variables. Hand execute your code with $\mathbf{n}=100$ to verify it calculates $\mathbf{m}=681,682$ or 683 using integer math.
(15) Question 7. A high-power LED requires 3.2 V at 100 mA to activate. Assume an ideal diode: $\mathrm{I}=0$ for $\mathrm{V}<3.2 \mathrm{~V}$, $\mathrm{I}=100 \mathrm{~mA}$ for $\mathrm{V}=3.2 \mathrm{~V}$, and $\mathrm{I}=$ infinite for $\mathrm{V}>3.2 \mathrm{~V}$. Interface this LED to the LM3S1968 port pin PG0, such that when the software outputs a one, the LED comes on, and when the software outputs a zero, the LED goes off. If more than one possibility exists, choose the cheapest method. Label all interface components and resistor values. Show the hardware interface (no software is required). You can specify resistor values using an equation, rather than calculating the exact number. You will choose whether to power it from the 3.3 or 5 V supply.

