

ALVIN W LEUNG

Local Address

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Permanent Address

311 Sanderling Ln.
Sugar Land, TX 77478

Education

Master of Science, Electrical and Computer Engineering, May 2009
The University of Texas at Austin

Bachelor of Science, Electrical and Computer Engineering, May 2007
The University of Texas at Austin
Overall GPA: 4.00/4.00
Major GPA: 4.00/4.00

Related Courses

Probability and Stochastic Processes, Microprocessor Applications and Organization, Intro to System Theory, Digital Signal Processing, Digital Communications, Information Theory, Advanced Wireless: Modulation and Multiple Access, Embedded and Real-Time Systems Lab, Real-Time Digital Signal Processing Lab, Digital Image and Video Processing, Multidimensional Digital Signal Processing, Wireless Communication Lab, Entrepreneurial Management, Computer Architecture

Experience

06/2008 - 08/2008

Systems Group Intern, Purewave Networks(Mountain View, CA)
Implement features of 802.16e-2005 WiMAX Simulator
Study effects of fixed point implementation on LLR calculations
Implement Convolutional Turbo Code encoder/decoder in C
Model and mitigate RF channel spur effects

06/2006 - 08/2006

Intern, Texas Instruments(Plano, Texas) - DLP Technology Development
Generated new verification plan for ASIC sub-block
Composed complete test suite for ASIC sub-block using Specman Elite
Created template for future verification code development

06/2005 - 08/2005

Intern, Texas Instruments(Plano, Texas) - DLP Front Projection
Supported Design Verification Testing Tool development
Composed automated test scripts for DVT
Facilitated front projection testing

06/2004 - 08/2004

Manager, Wok & Roll(Houston, Texas)
Managed daily operations
Maintained inventory for entire restaurant
Trained and monitored employees
Provided customer service and handled questions and complaints

Skills

High-level languages: C, C++, e
Assembly languages: TI TMS320C6000 VLIW DSP, Motorola 6812
Algorithm development environments: LabVIEW, MATLAB
Development environments: Metrowerks Codewarrior, TI Code Composer
Design automation/Verification tools: Multisim, PSpice, Modelsim, Specman Elite
Operating systems: Windows 98/2000/XP, Unix, Linux

Accomplishments

Recipient of Microelectronics and Computer Development (MCD) Fellowship (2007-2008)
Recipient of Virginia & Ernest Cockrell, Jr. Scholarship in Engineering (2003-2007)
Engineering Scholar (Top 5% of College)
Engineering Honors Program
University Honors; Fall '03 - Spring '07
Officer - Eta Kappa Nu Honor Society; Member - IEEE, Tau Beta Pi Honor Society

Employability Status: US Citizen/Permanent Resident