Modeling and Simulation of Discretized Data Transmission in Very High-Speed Digital Subscriber Line

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## Very High-Speed Digital Subscriber Line (VDSL)



- Data Encoder/Decoder: Quadrature Amplitude Modulation (QAM)
- Discrete Multitone (DMT): Using IFFT/FFT to generate orthogonal channels for optimal bit allocation [Chow91, 95]
- Cyclic Extension and Windowing: Intersymbol interference mitigation, alignment of symbols, suppression of sidelobes [ETSI99, 01]

## **VDSL Transmission Environment**

- Channel Modeling: *linemod* program from Stanford University
  - Model channel as a FIR filter
- Noise Modeling: A two-term Gaussian mixture model [Dai01]
  - Include both the crosstalks and impulse noise



## **Proposed Work**

- Model VDSL transceiver and the channel in Synchronous Dataflow (SDF)
  - Bit processing communication systems are best modeled by SDF
- Main open issue: Optimization of the transceiver design
  - Use Advanced Design System (ADS) software from Agilent to simulate the system and evaluate the bit error rate performance

## References

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