HETEROGENEOUS MULTIPROCESSOR MAPPING FOR REAL-TIME STREAMING SYSTEMS

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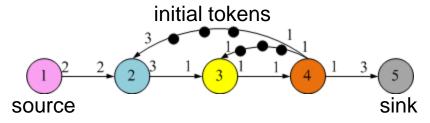
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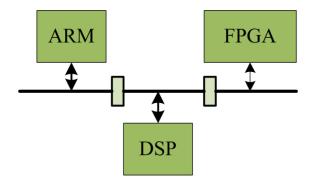


Introduction

Synchronous data flow (SDF) models



- Static schedule: 1-2-3-4-3-4-5
- Model for many real-time streaming applications, which desire high throughput and low latency
- Multiprocessor system-on-chips (MPSoCs)



Problem Definition

Mapping SDF models to MPSoCs

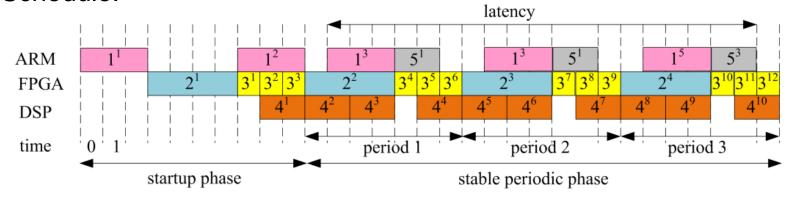
• Partition:

1 2 2 3 1 3 1 1 3 5

FPGA DSP

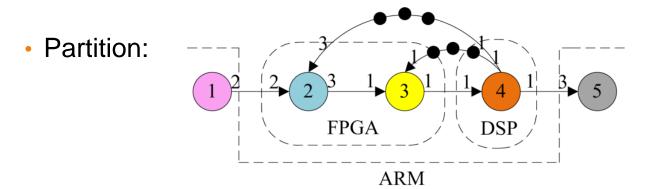
ARM

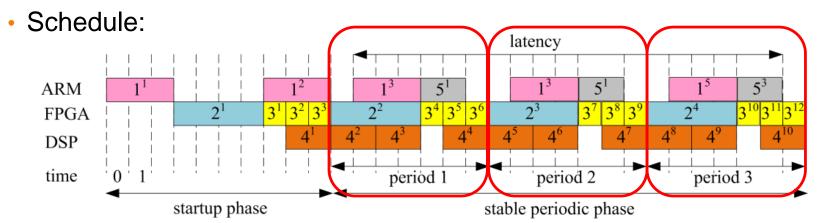
· Schedule:



Problem Definition

Mapping SDF models to MPSoCs





Period = 1 / Throughput

Latency = (End of the n-th exec. of Sink) – (Start of the n-th exec. of Source)

Prior Work

Publication	General SDF	Processor Heterogeneity	Objectives	Solution Form	Main Approach
[Lee1987]	Yes	No	Throughput	Single solution	Linear programming
[Bha1996]	Yes	No	Throughput	Single solution	Linear programming
[Zhu2009]	Yes	Yes	Buffer size	Schedule only**	Constraint programming
[Bon2010]	No*	Yes	Throughput	Single solution	Graph-based solution
[Zit2000]	Yes	No	Multiple	Pareto front***	Evolutionary algorithm

^{*} Homogeneous SDF graph only

^{**} Partition is assumed to be given

^{***} A set of points that are Pareto optimal

Our Goal

- Mapping general SDF to heterogeneous MPSoCs
- Multi-objective optimization
 - Throughput
 - Latency
 - Processor cost (e.g. price, area)

A multi-objective optimization framework that jointly optimizes throughput, latency and processor cost for general multiprocessor SDF mapping.

- An integer linear programming (ILP) Model
- Optimize partitioning and scheduling simultaneously
- Objective:

Minimize
$$\lambda_1 \cdot Period + \lambda_2 \cdot Latency + \lambda_3 \cdot Cost$$

- Constraints
 - SDF semantics
 - Static partitioning
 - Execution time profile
 - Sequential execution of actors mapped to the same processor
 - Stable periodic schedule

- Actor i; Processor j; Time $t \in \{0,1,...T\}$
- Decision variables
 - $S_i(t)$, $E_i(t)$: Number of started/ended executions of actor i up to time t
 - A_{ii}: Indicator of whether actor i is bound to processor j
 - start(t): Indicator of the start of stable periodic phase

Constraints

- Execution precedence: $c_{i_1,i_2}S_{i_2}(t) \le p_{i_1,i_2}E_{i_1}(t) + o_{i_1,i_2}$
- Execution time: $S_i(t) = \sum_j A_{ij} E_i(t + d_{ij})$
- Sequential execution: $\sum_{j} A_{ij}(S_i(t) E_i(t)) \le 1$
- Periodicity of the schedule: $W_i(T) \sum_t W_i(t) start(t) = n_i \sum_j A_{ij} d_{ij}$
- Definition of objectives: $Period = T \sum_t t \cdot start(t)$; $Cost = \sum_j Alloc_j \cdot pc_j$ $Latency = \sum_t (U(t) V(t)) + \sum_j A_{Ij} d_{Ij} + (S_1(T) S_I(T)) \cdot Period$ Time interval between Source's 1st start and Sink's 1st end in the periodic phase

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Linearize product terms: add one variable and three constraints

- Periodicity of the schedule: $W_i(T) \sum_t W_i(t) start(t) = n_i \sum_i A_{ij} d_{ij}$
- Definition of objectives: $Period = T \sum_{t} t \cdot start(t)$; $Cost = \sum_{j} Alloc_{j} \cdot pc_{j}$

Express indicator functions with two constraints

$$Latency = \sum_{t} U(t) - V(t) + \sum_{j} A_{Ij} d_{Ij} + (S_1(T) - S_I(T)) \cdot Period$$

$$\text{Time interval between}$$

$$\text{Source's 1}^{\text{st}} \text{ start and Sink's}$$

$$\text{Difference in iteration numbers}$$

1st end in the periodic phase

- NP hard!
- Actor i; Processor j; Time $t \in \{0,1,...T\}$
- Decision variables
 - $S_i(t)$, $E_i(t)$: Number of started/ended executions of actor i up to time t
 - A_{ij} : Indicator of whether actor i is bound to processor j
 - start(t): Indicator of the start of stable periodic phase

Constraints

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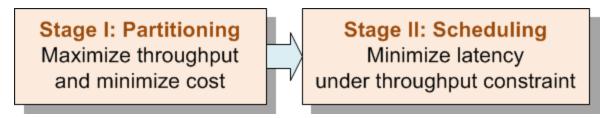
Express indicator functions with two constraints

 $Latency = \sum_{t} \underbrace{U(t) - V(t)}_{j} + \sum_{j} A_{Ij} d_{Ij} + \underbrace{(S_{1}(T) - S_{I}(T))}_{j} \cdot Period$ Time interval between $Source's 1^{st} start and Sink's$ Difference in iteration numbers

1st end in the periodic phase

Heuristic Optimization

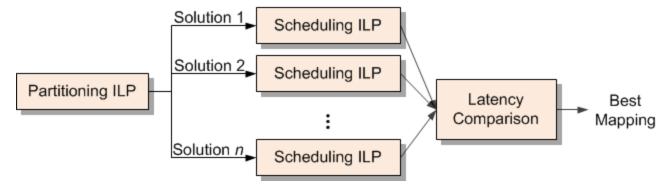
- Maximum throughput partition
 - For fixed partition, the best throughput is determined by the critical processor
 - Empirically, the best throughput is achievable given long enough startup phase and proper scheduling
- 2² 3⁴ 3⁵ 3⁶ Critical processor
- Just optimize partitioning for the best throughput and cost
- Two-stage optimization process



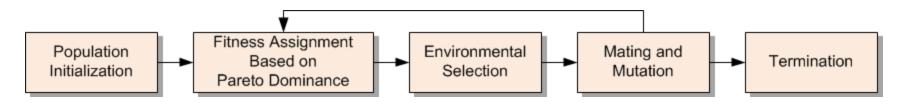
Throughput and cost are prioritized over latency

Heuristic Optimization

Two ILPs



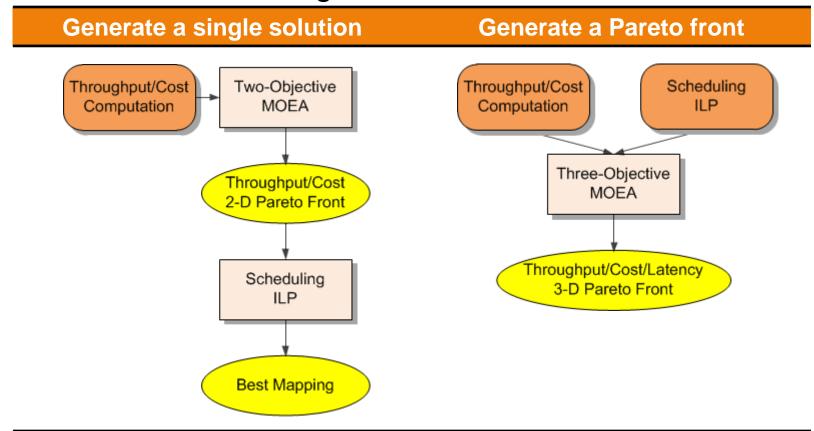
Multi-Objective Evolutionary Algorithm (MOEA)



- The population consists of a subset of all possible partitions
- Converge to a set of multi-objective optimal partitions, i.e. Pareto front

Heuristic Optimization

MOEA with Scheduling ILP



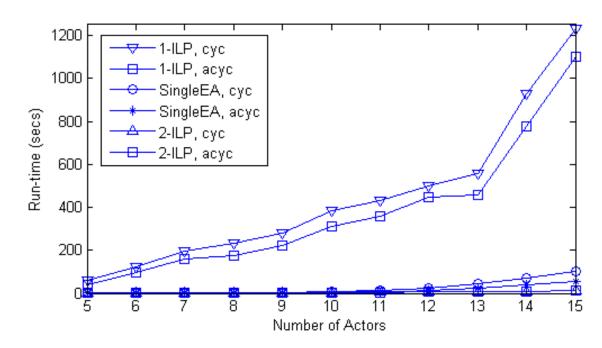
Global vs. Heuristic Optimization

	Global Optimization	Heuristic Optimization
Optimality	Global optimal	Sub-optimal
Computational Complexity	NP hard	MOEA: $O(M^2 log M)$ per iteration* Scheduling ILP: polynomial time
Design Space Exploration	Generate a single mapping; Generate a Pareto-front by fine tuning the weights	Generate a single mapping or a three-objective Pareto front

^{*} M is the population size

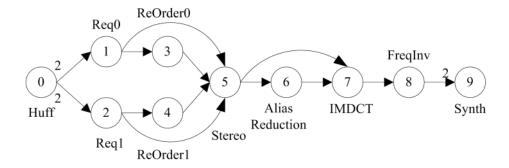
Experimental Results

- Programming Tools
 - ILP: CPLEX Concert Technology for C++
 - MOEA: MOGALib framework in C++
- Run-time comparison
 - random cyclic/acyclic SDF graphs mapped to 3 processors



Experimental Results

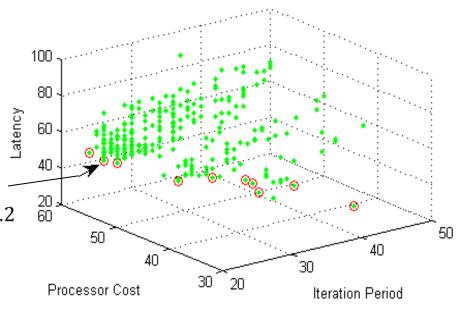
Design space exploration for an MP3 decoder



Convergence to Pareto front

~1 hour execution time

Solution of global ILP with $\lambda_1 = 0.8$ and $\lambda_2 = 0.2$



Conclusion

- Mapping SDF models onto heterogeneous MPSoCs
- Global ILP
- Heuristics by MOEA
- Generate a single mapping or a Pareto front

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Thank you for your attention!

