

Professor: Alexis Kwasinski, ENS528, 232-3442, akwasins@mail.utexas.edu

Office Hours: Dr. Kwasinski's office hours for this course are M 1:30-2 and 3-4:30 and W: 12-2. And, he is often in the power lab during the scheduled lab sessions.

Course Web Page: Materials will be posted on the course webpage
(<http://users.ece.utexas.edu/~kwasinski/EE462LS11.html>)

Reference Textbooks (recommended, but not required): N. Mohan, T. M. Undeland, W. P. Robbins, *Power Electronics: Converters, Applications, and Design*, Third Edition, John Wiley & Sons, Inc., 2003. P. T. Krein, *Elements of Power Electronics*. New York and Oxford: Oxford University Press, 1998.

Description: Analysis, design, and operation of power electronic circuits. Emphasis on single-phase power conversion from AC to DC, DC to DC, DC to AC, and maximizing the power from photovoltaics (PV). Design and construction of 150W power electronic circuits in the power laboratory, and comparison of their performance to theory. A working circuit model will be available for observation, study, and improvement. Use of the ECE machine shop is not required, but those of you who want to use the shop are welcome to do so. But, to use the shop, you must first be "machine shop certified." See ECE technicians Paul Landers or Daryl Goodnight about shop certification and use.

Detail Course Description: It can be found in the course description file which can be downloaded from the course web page.

Class, Lab Sessions, and Work Schedules:

- Lectures on MWF 2-3, in ENS 115.
- Lab sessions in the power lab, ENS212:
 - Sections 16775 and 17146: Thur. from 9:30 am to 12:30 pm
 - Sections 16780 and 17140: Thur. from 3:30 pm to 6:30 pm
 - Sections 16785 and 17145: Wed. from 7:00 pm to 10:00 pm
- Use lab stations 15-22 during your scheduled lab time. These are the stations without PCs, so bring a hardcopy of the lab document. The hardcopy is useful for making notations as you proceed. You can also use the black cabinet tops, which are handy for construction and soldering.
- The group lab session times shown above may or may not be sufficient to complete your projects. Work with your assigned partner for that project to meet other times as needed during the regular 2nd floor ENS undergraduate teaching lab hours.
- If you have a highly constrained schedule (work hours, sports, Longhorn band, etc.) that will make it difficult to coordinate work times with a partner, or if there are good reasons that you should work by yourself, discuss with Dr. Kwasinski the possibility of being a *solo* student (i.e., no partner) for the semester.
- Regarding conduct, it will be appreciated if students are 1. on-time, 2. refrain from eating, drinking, and disturbing others, and 3. conduct themselves in a manner consistent with a professional environment.
- Cell phones and laptops: In class, turn them off (unless you clear it with Dr. Kwasinski). In lab, laptops are OK.
- In some few weeks when additional time for completing the project is needed, lab work will replace either lecture times.
- Group arrangements and projects format or schedule may be modified due to unforeseen logistical issues. Any such changes will be posted online and announced in class.

TAs:

Deepak Mohan and Seunghyun Chun.

Prerequisites: EE438 or EE331 with a grade of at least C.

Grading Formula: Lab circuits and reports -----50%
Two tests -----20%
(note – one 8½ x 11” sheet of notes is permitted at each test)
Comprehensive final exam (according to the official U.T. schedule - Friday,
May 13, 2:00–5:00 pm) -----20%
(note – one 8½ x 11” sheet of notes is permitted at the final exam)
Attendance (taken at all lab sessions, and at most lectures)-----10%

Fractional letter grade (A, A-, B+....) will be used for the final grade.

Disclaimers and general policies:

Although unlikely, this syllabus and course topics may change according to my judgment as to what is best for the class. Any changes will be declared in class.

General course schedule and administrative deadlines follow The UT Austin calendar, long session 2009-2010, which can be found at <http://registrar.utexas.edu/calendars/10-11/>

Any student with a documented disability who requires academic accommodations should contact Services for Students with Disabilities at 471-6259 (voice) or 232-2937 (Video Phone) as soon as possible to request an official letter outlining authorized accommodations.

The core values of The University of Texas at Austin are learning, discovery, freedom, leadership, individual opportunity and responsibility. Each member of the University is expected to uphold these values through integrity, honesty, trust, fairness, and respect toward peers and community. Students who violate University rules on scholastic dishonesty are subject to disciplinary penalties, including the possibility of failure in the course and/or dismissal from the University. Since such dishonesty harms the individual, all students, and the integrity of the University, policies on scholastic integrity will be strictly enforced. Academic misconduct including, but not limited to, plagiarism and/or copy previous years reports will be treated according to the university policy detailed in <http://deanofstudents.utexas.edu/sjs/>. I keep copies of previous years reports. So be wise!! Besides the moral issues, a few more points added to your grade does not worth the risk and consequences of being caught cheating.

University policy on course drop dates states: “A student may not drop a course after the twelfth class day in the long session except for good cause (e.g., health or serious personal problems, or a demonstrated need to work more hours). A graduate student seeking to drop a class after the twelfth class day should go to the department offering the course.” For this course, the department offering the course is ECE.

University policy on holy days states: “A student who misses classes or other required activities, including examinations, for the observance of a religious holy day should inform the instructor as far in advance of the absence as possible, so that arrangements can be made to complete an assignment within a reasonable time after the absence.”

Schedule:

Week	Lecture Periods	Lab Periods
Jan. 17 (Mon. is a holiday)	Course overview. Basic circuit components.	Orientation. Soldering demonstration and construction tips.
Jan. 24	Basic circuit components (continue). SCRs, triacs, and light dimmers. <i>Dr. K at GBPF (M – W).</i>	Assign Light Dimmer and Teams #I. The team writes one report, but each team member builds and demonstrates his/her own circuit
Jan. 31	Transformers, diode bridge rectifiers (DBR). Waveforms and definitions.	Tool check. Assign DBR and continue with Teams #I
Feb. 7	Waveforms and definitions. (cont.) Photovoltaics and renewable energy. MOSFETs and MOSFET firing circuits.	Light dimmer circuit and report due. Review DBR progress. Assign Solar and Teams #II. The solar lab does not require lockers. Do not rotate lockers. Show your hand plotted solar data on the lab document graph to Dr. Kwasinski or the TAs for his visual inspection before March 11.
Feb. 14	MOSFETs and MOSFET firing circuits (cont). DC-DC buck converters. <i>No lecture class on W.</i>	DBR circuit and report due. Tool check. Assign MOSFET firing circuit and Teams #III. Rotate lockers.
Feb. 21	Boost Converter	MOSFET Firing Circuit and report due. Assign Buck Converter and continue with Teams #III.
Feb. 28	DC-DC SEPIC (converter). Test #1 on Monday	Buck Converter circuit and report due. Assign Boost Converter and continue with Teams #III.
Mar. 7	PI controller for DC-DC boost converter. <i>Dr. K at APEC (M – W)</i>	Boost Converter circuit and report due. Assign SEPIC (converter) and continue with Teams #III.
Mar. 14	Spring Break	Spring Break
Mar. 21	H-bridge inverter – basics and unipolar PWM controller.	SEPIC Converter circuit and report due. Assign PWM Controller and Teams #IV. Rotate lockers.
Mar. 28	H-bridge inverters – isolated firing circuits.	Continue with Teams #IV and ongoing projects.
Apr. 4	H-bridge inverters – bridge section and output filtering. <i>Dr. Kwasinski at IEEE Systems Conference.</i>	PWM Controller circuit and report due. Assign H-Bridge inverter circuit and continue with Teams #IV.
Apr. 11	H-bridge inverter – audio amplifier. <i>On Monday, Dr. Kwasinski at Electric Ship Symposium</i>	Solar report due. Assign Audio Amplifier and continue with ongoing project with Teams #IV.
Apr. 18	H-bridge inverter – renewable power to grid..	Continuation of on-going project.
Apr. 25	Motor drives and Reliable power. Test #2 on Monday	H-Bridge Inverter report due. Start Power to Grid and continue with Teams #IV.
May 2	Additional applications and course evaluation	Audio Amplifier report due. Equipment check-in during the usual lab periods. Attendance in lab this week counts double. Power to Grid report due in Dr. Kwasinski's office door slot anytime before Friday, May 6.

Notes – Unless told otherwise, reports are due at the beginning of your lab period (or, in some instances, during the following Monday's class). Hardcopy only. Partners are assigned randomly within sections, with no repeats. Please do not request partners except for the following: partner requests for Teams #III and #IV will be considered if both partners sign and submit their request in writing (no email) in class on the Monday prior to the assignment date. Partners for Team #III cannot repeat as Team #IV. Partners must be in the same lab session.