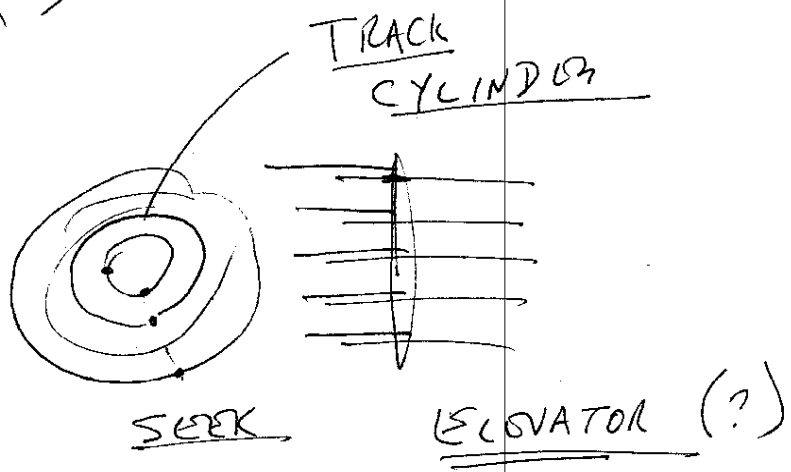


Memory-mapped / Specific int.  
Asynchronous (cpu, ~~to~~ I/O)  
 - Synchronize

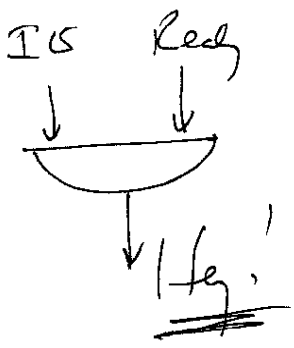
I/O/1

I/O Device  
 Hardware  
 Medium  
 Transducer  
Control



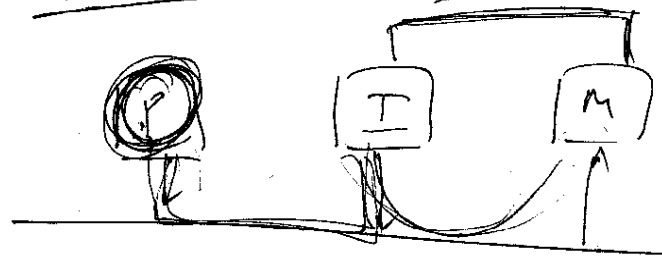
Polling / ~~I/O~~ Interrupt

I/O  
Control /  
I/O Computer



→ I/O Control Block

DMA Access



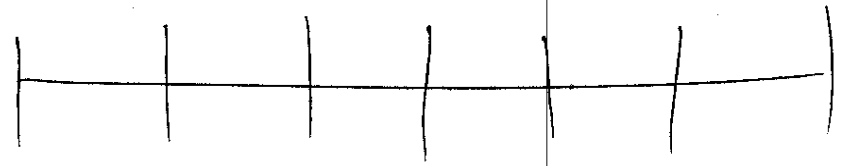
CYCLE  
STOALING

Asynch / Sych

Handshaking

Master / Slave

Ethernet

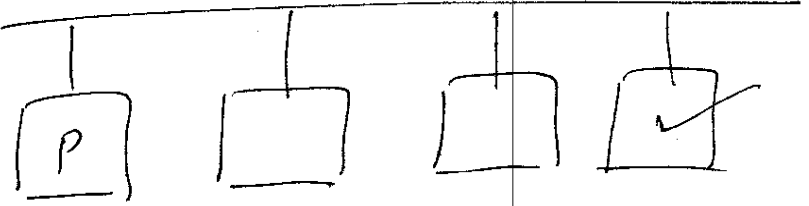


BU12ST

Bus



Multiplexed



Transfer

MS

SS

CC

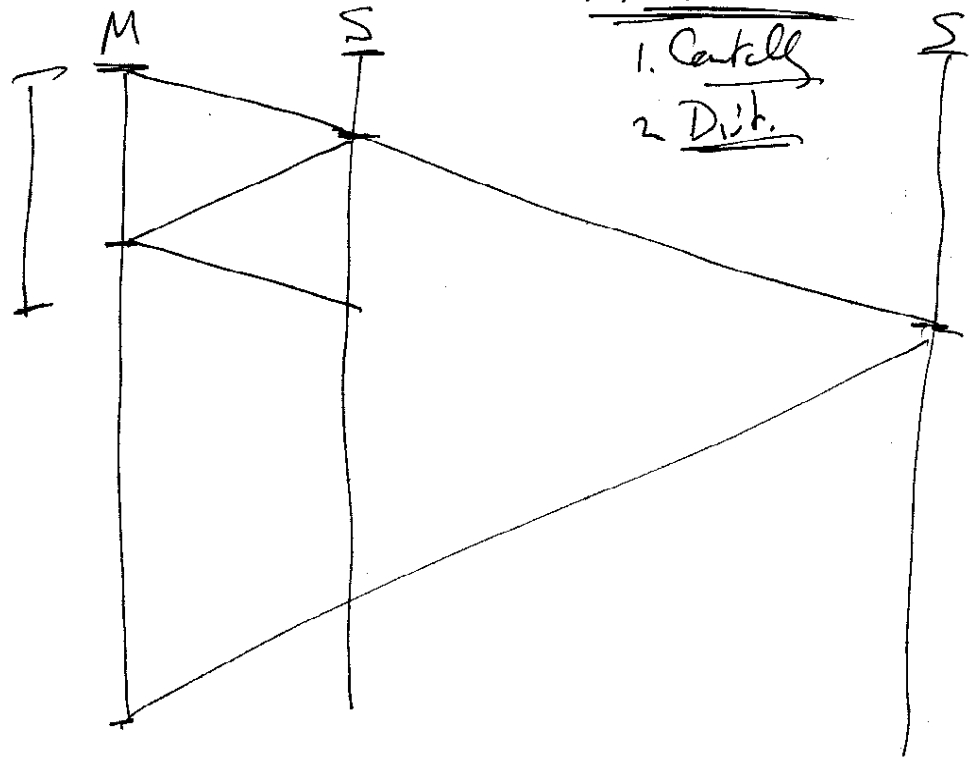
BBSY

Arbitration

1. Contell

2. Dist.

transf



TIME

