

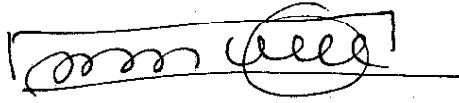
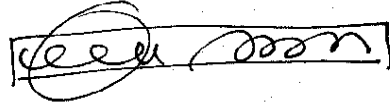
Condition Codes (C, N, V, Z) if (A=B)

Memory-mapped I/O

Fixed-length

Endian →

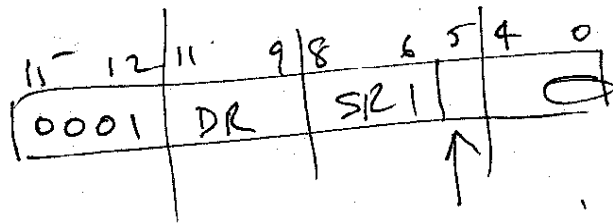
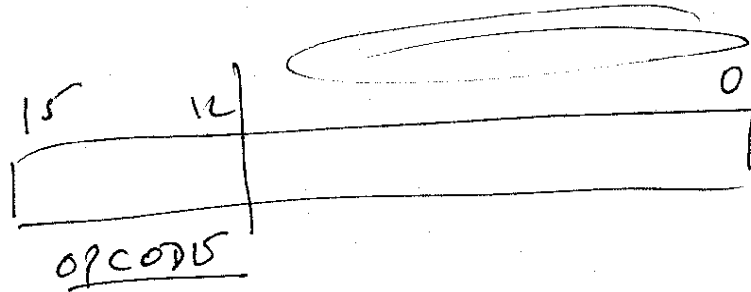
Word length



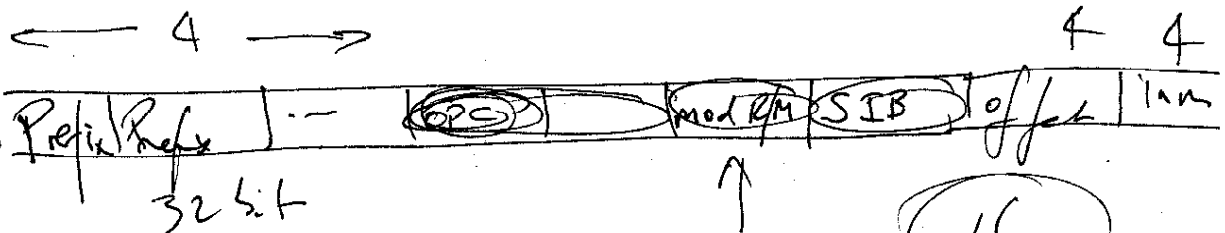
Operands (ADD)

data movement (LD)

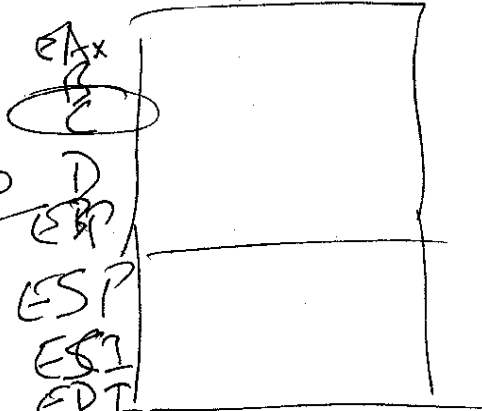
control (Branch)



Steering bit



432
Can I
CDE6600



110000 -16

001111
1
010000 16

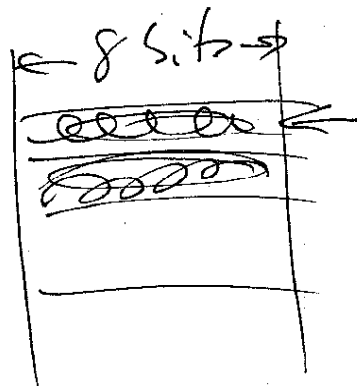
110000 ←
001111 ←
111111
1
000000

110000
010000
000000



110000
111110 -2
000001 -2
000010 2

Address Space = 16
Addressability 8 bit



B1700

bit-addressable

CDC 6600
60

CRAY I
64