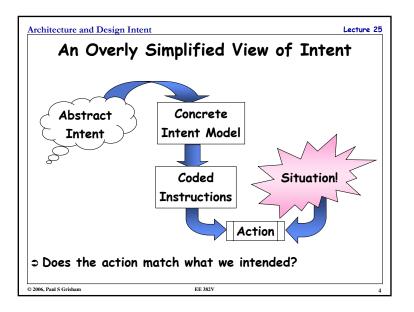
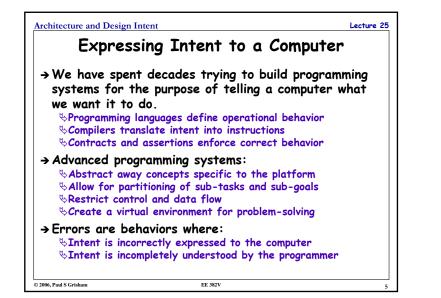
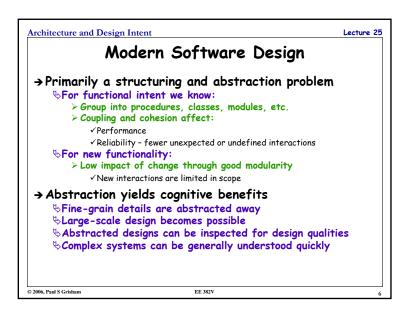
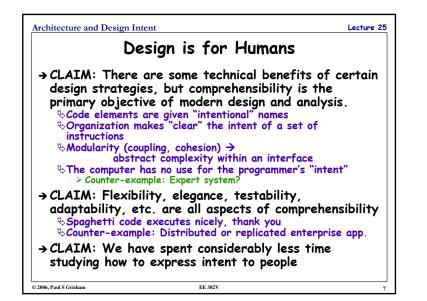


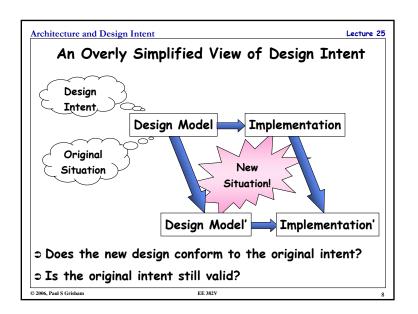
			An Anecdote
off	lds movb bclr	- \$F000 #\$0C00 #\$80, \$0002 \$0000,#\$80	→ RPI instruction tells the processor to READ PROGRAMMER
look loop	anda	cnt \$0000 #\$7F	INTENT
	bne	off cnt cnt loop \$0000,#\$80	→ Wouldn't it be nice if the computer could understand what we are trying to do?
key	org	%00100011 \$FFFE main	

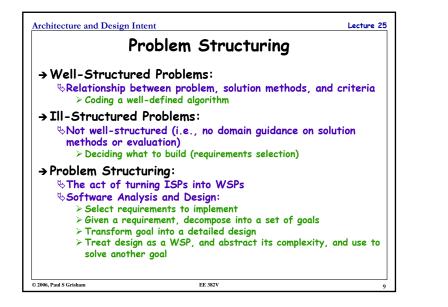


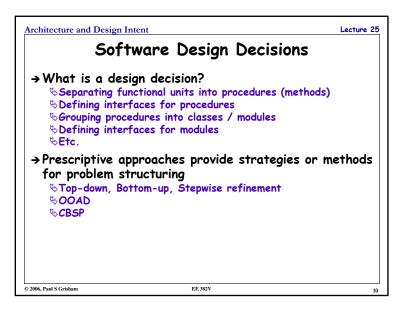


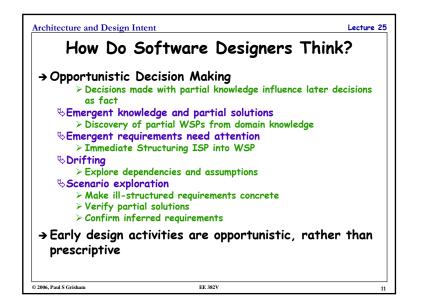


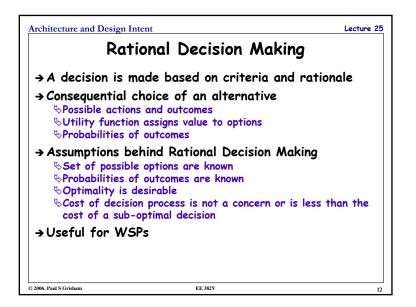


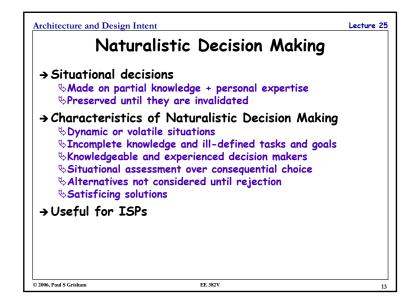


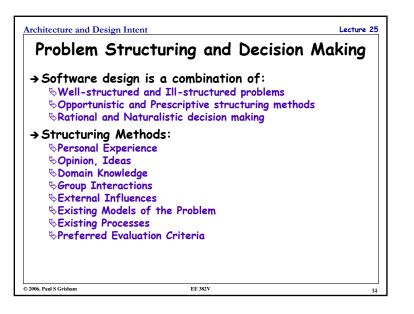


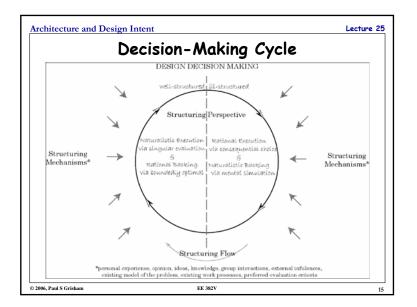


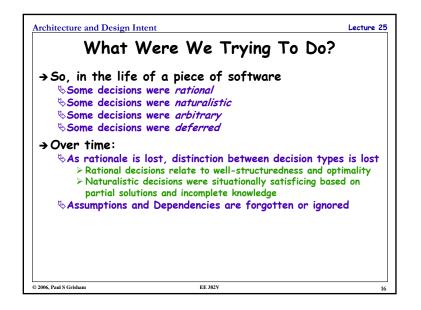


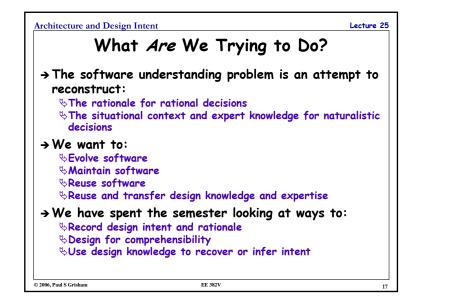


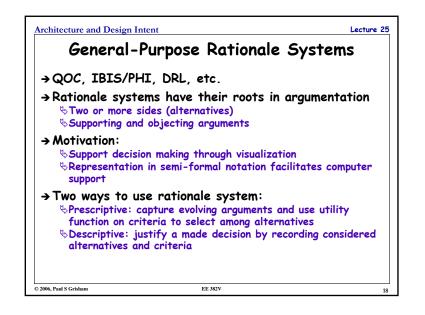


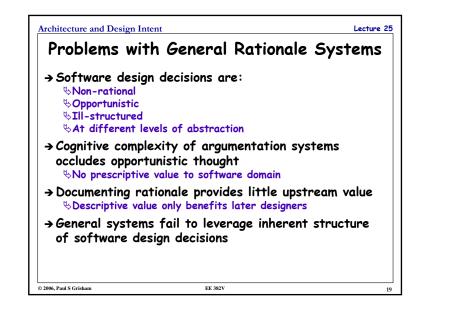


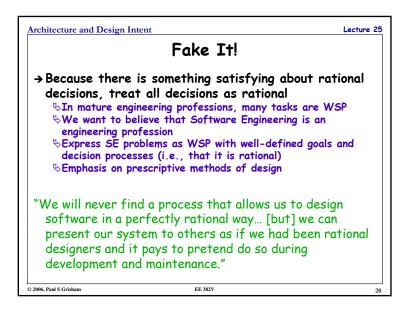


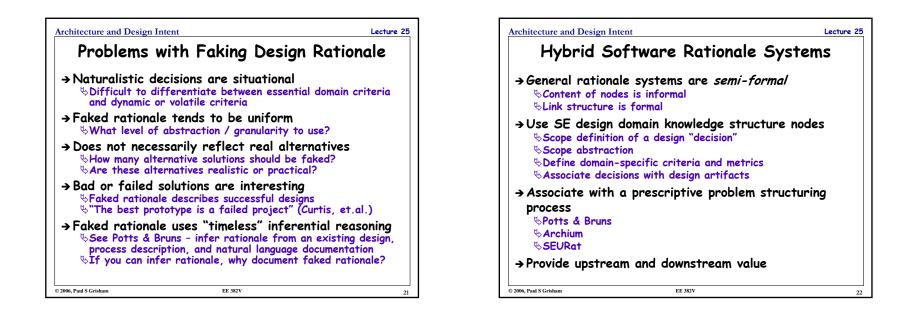


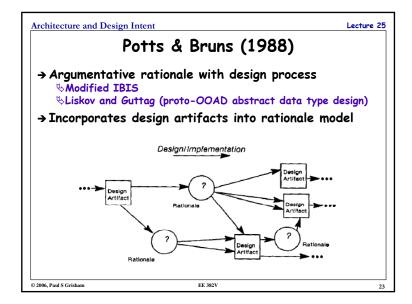


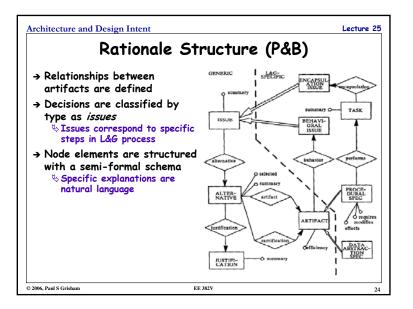


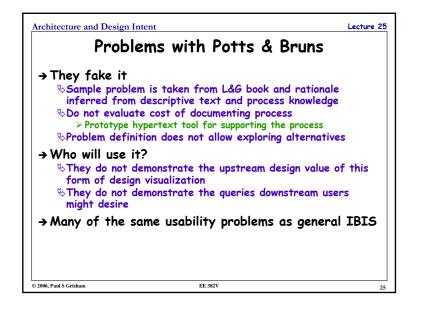


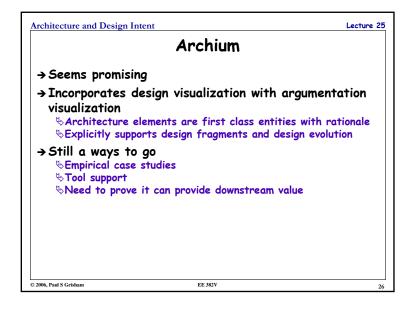


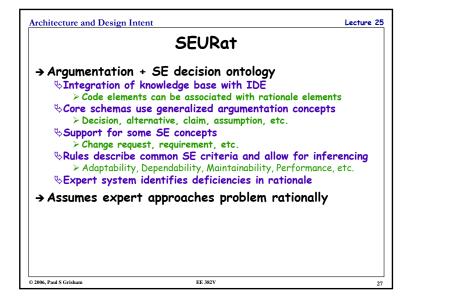


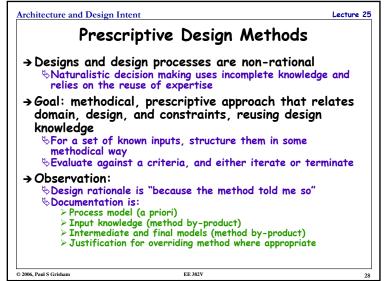


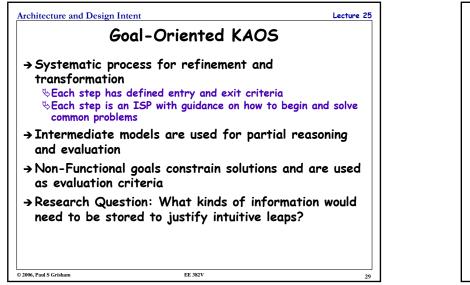


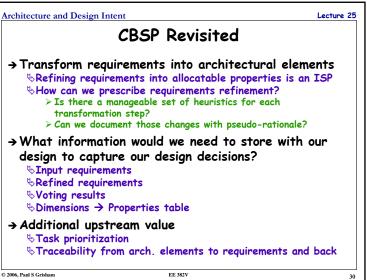


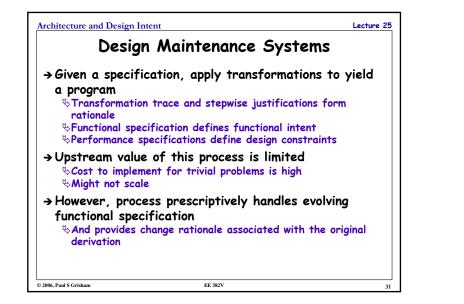


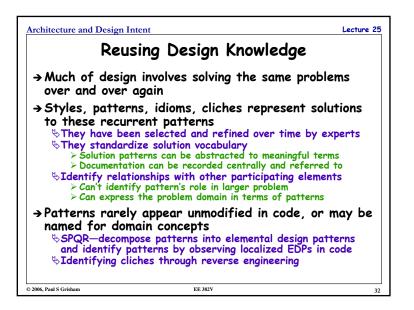


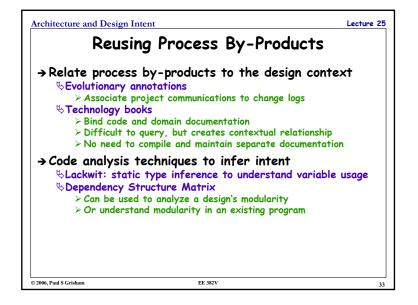


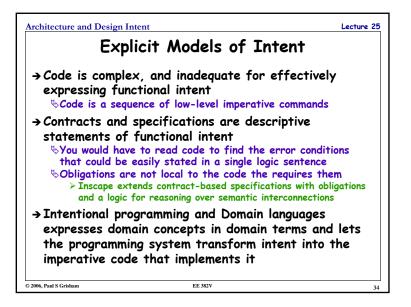


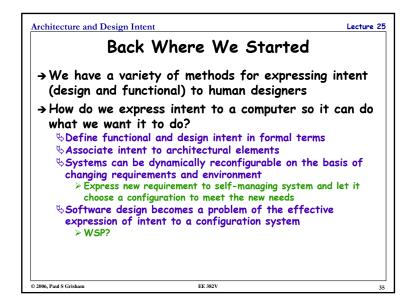


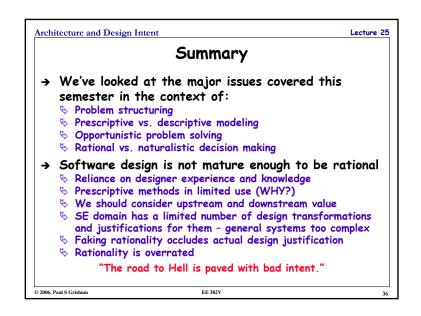












Architecture and Design Intent	Lecture 25				
Credits					
→C. Zannier and F. Maurer. Decisions in Agile D (Submitted to FSE'06))esign.				
→ R. Guindon. Designing the Design Process: Expl Opportunistic Thoughts.	oiting				
→C. Potts and G. Bruns. Recording the Reasons Design Decisions.	for				
→ D. Parnas and P. Clements. A Rational Design Process: How and Why to Fake it					
→ J. Grudin. Evaluating Opportunities for Design Capture					
→ Everybody's very fine presentations and All the other papers we've covered this semester!					
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