Errata for Fundamentals of Logic Design, 5th ed, hardcover
(3rd, 4th, and 5th printings)

Look on the back of the title page of the textbook (the copyright page) and you will find a line that reads either 5 6 7 08 07, 4 5 6 7 08 07 06 05, 3 4 5 6 7 06 05 04, 2 3 4 5 6 7 06 05 04 03, or 1 2 3 4 5 6 7 06 05 04 03. If the line begins with 1, you have the first printing of the text, and you should print out an errata list from www.ece.utexas.edu/~roth/FLD5/FLD_Errata1.pdf. If the line begins with 2, you have the second printing of the text, and you should print out an errata list from www.ece.utexas.edu/~roth/FLD5/FLD_Errata2.pdf. If the line begins with 3 or 4, you have the third or fourth printing of the text, and you should use this errata list. If the line begins with 5, you have the fifth printing of the text, and only the errors marked with a * apply. Note that the list is divided into two parts. Be sure to check both parts of the list before reporting any errors.

Reward: Dr. Roth (ENS 510) will pay a $5 reward to the first person who finds any additional technical error in the text. He will pay a $2 reward to the first person who finds any additional minor error (spelling, grammar, etc.). Please verify your error with a T.A. and then send it to roth@ece.utexas.edu.

"s.b." means "should be"

p. 20, line –6: 1011 s.b. 1010
p. 221, Prob. 8.J: line 2, insert after AB: (00, 01 or 10)
p. 250, 2nd line above Fig. 9-34: a'b'c'd' s.b. a'b'c'd
*p. 275, Fig. 10-13: add before line 1:

library BITLIB;
use BITLIB.bit_pack.all;

p. 312, figure at bottom left: Clear s.b. ClrN
*p. 329, Figure 12-7(b): add an arrow from the Q1 waveform to the Q0 waveform (starting at the last vertical dashed line)
*p. 341, Fig. 12-24: P s.b. Clock
p. 426, part (d): S2 ≠ S3 s.b. S2 ≠ S3
p. 436, 4th line after Definition 15.1: length 5, length 6 s.b. length 4, length 5
p. 476, 8th line of Section 16.4: 13-18 s.b. 13-19

Formatting, spelling, and grammatical errors:

p. 23, Prob. 1.3: delete (a)
p. 120, Section 5.1, line 4: sum-of-product s.b. sum of product
p. 307, Fig. 11-24, right flip-flop: add small arrow at Ck input
p. 331, Fig. 12-10(a): Input s.b. Input
p. 400, line –11: active/edge clock s.b. active clock edge
p. 438, line 8: a = d s.b. a ≡ d
p. 444, line 13: the a KA s.b. the KA
*p. 663, line –4: insert ; after end process
*p. 665, line –8: or s.b. or