Lab 5 grading sheet Students name Use same spelling as list	1) ed on Bla	Last_		First	EID
Students name	2)	Last_		First	EID
Circle instructor:	Valvano Telang Yerraballi Gerstlauer		TTh5 MWF2 TTh3:30 or MW TTh2	3	
1. Deliverables 20%		uuuu	11112		
0) This sheet					
,		nents in	this order into o	ne doc docx o	r pdf file and upload it to
Blackboard before your 1) A sequence in sin 2) Circuit dia draw 3) Drawing co	<i>checkou</i> e of fou nulation gram (v it so it of the fir	<i>ut time.</i> r to six mode v vith you looks lil	Have this file open screen shots screen when cars are prese or name and date). ke Fig. 3.1	n on the compute n shots showing ent on both road	<i>er during demonstration.</i> g the system running s.
2. Performance 40% Does it handle con How pretty is the	rectly a		ions as specified?	1)	2)
				,	,
3. Demonstration 40	%:				

During checkout, you will be asked to show both the simulated and actual 9S12 systems to the TA. The TAs will expect you to know how the **Timer_Wait** function works, and know how to add more input signals and/or output signals. An interesting question that may be asked during checkout is how you could experimentally prove your system works. In other words, what data should be collected and how would you collect it? If there were an accident, could you theoretically prove to the judge and jury that your software implements the FSM? What type of FSM do you have? What other types are there? How many states does it have? In general, how many next-state arrows are there? Explain how the linked list is used to implement the FSM. Explain the mathematical equation used to calculate the address of the next state, depending on the current state and the input. Be prepared to write software that delays 1 second without using the timer (you can use a calculator and manual). How do you prove the delay will be 1 second? Explain the difference between the **blt** and **blo** instructions. List some general qualities that would characterize a good FSM

