

EE319K Introduction to Embedded Systems

- Course description
- Introduce embedded microcomputer systems,
- Flowcharts,
- Data flow graphs,
- Call graphs.

Three good data sheets to have available

<http://users.ece.utexas.edu/~valvano/Datasheets/CPU12rg.pdf>

<http://users.ece.utexas.edu/~valvano/Datasheets/S12CPUV2.pdf>

<http://users.ece.utexas.edu/~valvano/Datasheets/MC9S12DP512.zip>

An *embedded computer system* includes a microcomputer mechanical, chemical and electrical devices specific dedicated purpose, and packaged up as a complete system.

- communications,
- automotive,
- military,
- medical,
- consumer,
- machine control.

Each **embedded microcomputer** system

accepts inputs,
performs calculations, and
generates outputs
runs in “real time.”

In a **real time system**, upper bound on the time required to perform the input/calculation/output respond to external events

Because of the real time nature of these systems, we will study the rich set of features built into these microcontrollers to handle all aspects of time.

1.2. Attitude

clients

coworkers

- Test it now.
- Plan for testing.
- Get help.

There is just no simple way to get to the moon.

Use our creativity to break a complex problem into simple components, rather than developing complex solutions to simple problems.

1.3. Components of an embedded system

embedded "hidden inside so one can't see it."

computer

processor

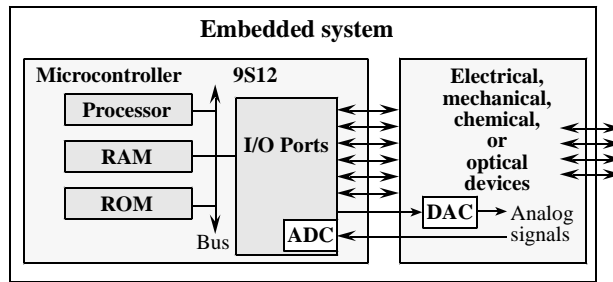


Figure 1.1. An embedded system includes a microcontroller interfaced to external devices.

Address	Size	Device	Device	Contents
\$0000 to \$03FF	1 Kib	I/O ports	Input/output devices	Access external devices
\$0400 to \$07FF	1 Kib	EEPROM	Electrically erasable PROM	Fixed constants
\$0800 to \$3FFF	14 Kib	RAM	Random Access Memory	Variables and stack
\$4000 to \$FFFF	48 Kib	EEPROM	Electrically erasable PROM	Programs and fixed constants

Table 2.3. The 9S12DP512 has 512 Kibibytes of EEPROM and 14 Kibibytes of RAM.

microcomputer
microcontroller
interface

- parallel - data is available simultaneously on groups of lines
- serial - binary data is available one bit at a time on a single line
- analog - data is encoded as a variable voltage
- time - data is encoded as period, frequency, pulse width or phase shift

nonvolatile
port
device driver

1.4. Flowcharts

Example 1.1. Design a flowchart for a system that performs two independent tasks. The first task is to output a pulse on **PTT** every 1.024 ms in real time. The second task is to find all the prime numbers, and there are no particular time constants on when or how fast one finds the prime numbers.

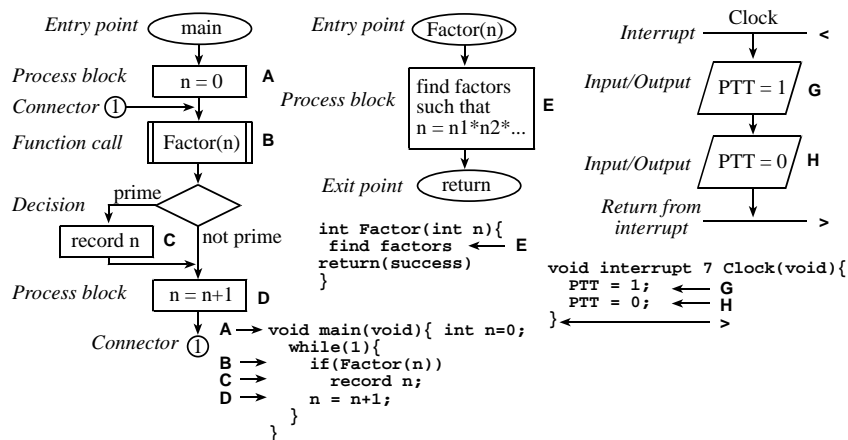


Figure 1.2. Example flowchart showing some common flowchart symbols.

structured programming.

- sequence,
- conditional
- while-loop
- fork-join
- interrupt

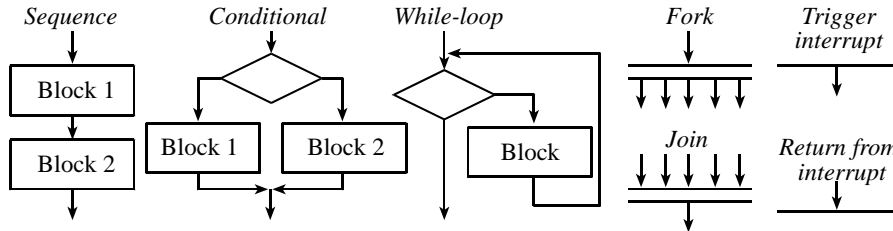


Figure 1.3. Flowchart showing the basic building blocks of structured programming.

1.5. Product development cycle

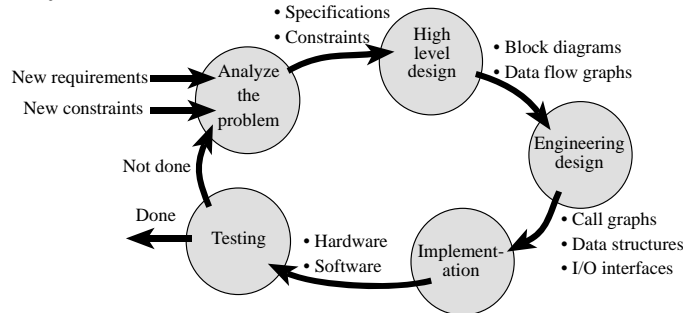


Figure 1.5. Software development cycle.

data flow graph (dependency graph)

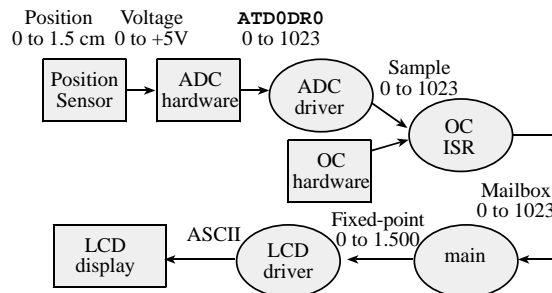
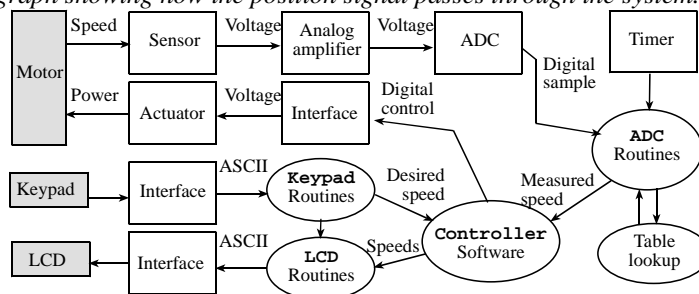


Figure 1.6. A data flow graph showing how the position signal passes through the system.



A data flow graph showing how signals pass through a motor controller.

Call-graphs

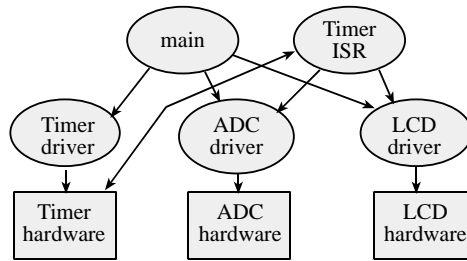


Figure 1.7. A call flow graph for a simple position measurement system.

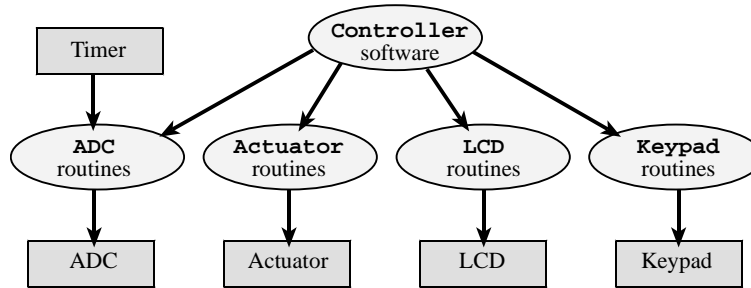


Figure 1.10. A call flow graph for a motor controller.

Testing

validate basic functions
 static efficiency (memory requirements),
 dynamic efficiency (execution speed),
 accuracy (difference between truth and measured), and
 stability (consistent operation.)

Maintenance

correcting mistakes,
 adding new features,
 optimizing for execution speed or program size,
 porting to new computers or operating systems, and
 reconfiguring the system to solve a similar problem.

Golden Rule of Software Development

Write software for others as you wish they would write for you.

1.6.2. Qualitative Performance Measurements

Can we prove our software works?

Is our software easy to understand?

Is our software easy to change?

self-documented code,

abstraction,

modularity, and

layered software.

You can tell if you are a good programmer if

1) you can understand your own code 12 months later,

2) others can make changes to your code.

The bottom line

Include definitions of terms in your technical comm.

RAM is expensive in an embedded system

Data flow graph is used during design phase