## EE345M Quiz 1 Fall 2003 Solution

(25) Question 2. The only writes to these two globals occur during the first microseconds of the first launch to server. There are no synchronization/corruption problems caused by reading the globals. Therefore no semaphores are needed, and the program will run as is.

(20) Question 3. The thread switch system with a status field added to implement blocking. (10) Part a) The error occurs because the instructions sts 2, x lds 2, x access the Next field instead of accessing the StackPt field as intended.

(10) Part b) The correction is to change the accesses to StackPt

```
void threadSwitchISR(void){
asm(" ldx _RunPt\n"
" sts 4,x");
RunPt = RunPt->Next;
PORTJ = RunPt->Id;
TC3 = TCNT+TimeSlice;
TFLG1 = 0x08;
asm(" ldx _RunPt\n"
" lds 4,x");
```

```
}
```

(20) Question 4. Lab 17 measured a time-jitter. It was usually a small number.

(10) Part a) We can define time-jitter,  $\delta t$ , as the difference between when a periodic task is supposed to be run, and when it is actually run. The goal of a DAS is to start the ADC at a periodic rate,  $\Delta t$ . Let  $t_n$  be the nth time the ADC is started. In particular, the goal to make  $t_n - t_{n-1} = \Delta t$ . The jitter is defined as the constant,  $\delta t$ , such that

 $\Delta t - \delta t < t_i - t_{i-1} < \Delta t + \delta t \qquad \text{for all } i.$ (10) Part b) Let dV/dt be the maximum slew rate of the input.  $\delta V = (dV/dt)^* \delta t$ 

(15) Question 5. There is a read-modify-write critical section involving the semaphore counter. If the semaphore is equal to 1, and two threads try and call OS\_Wait, the proper action is to let the first one pass and make the second one spin. If OS\_Wait does not disable interrupts, then is it possible for both threads to proceed.