Errata as of 11/9/2012
Embedded Systems: Introduction to ARM® Cortex™-M3 Microcontrollers
Second edition

Page 67 add use to "could use"

Page 78 change
In contrast, a general purpose computer the loader typically reads the object code from a file on a hard drive or CD and stores the code in RAM.
To
In contrast, the loader on a general purpose computer typically reads the object code from a file on a hard drive or CD and stores the code in RAM.

Page 91
E.g., to left shift the value in *M* by *N* bits and store the result in *R* we execute: $R = M<<N$.

Page 162
Using the minus sign makes the format is left justified, otherwise it is right justified.

Page 332, delete one of these paragraphs (it is repeated)
To use any of the features for a digital I/O port, we first enable its clock in the Run Mode Clock Gating Control Register 2 (RCGC2). For each bit we wish to use we must set the corresponding **DEN** (Digital Enable) bit. To use a pin as regular digital input or output, we clear its **AFSEL** (Alternate Function Select) bit. Setting the **AFSEL** will activate the pin's special function (e.g., UART, I^{2}C, CAN etc.) For regular digital input/output, we clear **DIR** (Direction) bits to make them input, and we set **DIR** bits to make them output.

Page 165 (remove are twice)
4.1 What parallel ports are available on the LM3S811?

4.2 What parallel ports are available on the LM3S1968?

Page 352
> **Checkpoint 10.3:** What range of frequencies is represented in the digital samples when the ADC is sampled once a second, like Figure 10.1?

Page 438
```
  MOVS  R10, R12     ; R10 = R12, N and Z flags get updated
```