Errata as of 5/19/2012
Embedded Systems: Real-Time Interfacing to the ARM® Cortex™-M3

Page 26, 5 lines from bottom, add a, making it “one page at a time”.

Page 35, caption for Figure 1.19 “output is high” to “output is low”

Page 77, second paragraph, change “A third approach is illustrated in Figure 2.9.” to “A third approach is illustrated in Figure 2.10.”

Page 79, caption for Table 2.7 should be LM3S8962 instead of LM3S1968

Page 86, 9 lines from bottom, change Figure 2.15 to Figure 2.14.

Page 95. Change
Figure 2.21. The carry bit is set on addition and subtraction when crossing the 255-0 boundary.
To
Figure 2.21. The carry bit is set on addition when crossing the 255-0 boundary. The carry bit is cleared on subtraction when crossing the 255-0 boundary.

Page 96 Change
If the two inputs to an addition or subtraction operation are considered as unsigned, then the C bit (carry) will be set if the result does not fit. In other words, after an unsigned addition, the C bit is set if the answer is wrong.
To
If the two inputs to an addition operation are considered as unsigned, then the C bit (carry) will be set if the result does not fit. In other words, after an unsigned addition, the C bit is set if the answer is wrong. If the two inputs to a subtraction operation are considered as unsigned, then the C bit (carry) will be clear if the result does not fit.

Page 96 Add line over top of C equation after subtraction
C: unsigned overflow  
\[ C = \overline{X_{31}} \cdot M_{31} \oplus M_{31} \cdot R_{31} \oplus R_{31} \cdot \overline{X_{31}} \]

Page 276, change
For edge-triggered PC4, the trigger flag is bit 2 of the GPIO_PORTC_RIS_R register. This flag can be cleared by writing a 4 to GPIO_PORTC_ICR_R.
To
For edge-triggered PC4, the trigger flag is bit 4 of the GPIO_PORTC_RIS_R register. This flag can be cleared by writing a 0x10 to GPIO_PORTC_ICR_R.

Page 437, 4th line from the top
Change
The ADC_RIS_R register has interrupt arm bits.
To
The ADC IM_R register has interrupt arm bits.
Page 441, program 8.9, change the type of $x$, and $d$ from unsigned short to short, because the slope may be negative.

Page 507. Example 10.4, change “plus 2.5V” to “plus 1.65V”.

Page 548, Solution to Checkpoint 2.1, change 0x2220.0074 to 0x2202.0074.

Page 548, Solution to Checkpoint 2.2, change 0x2220.0050 to 0x2202.0050.

Page 550, Solution to Checkpoint 3.12, size is 256, not 100.

Checkpoint 3.12: 

\[
\text{AddIndexFifo(F1, 256, short, 0, 1)} \\
\text{AddIndexFifo(F2, 256, short, 0, 1)}
\]

Page 550, Solution to Checkpoint 3.18, change 20 to 08.

\[
\text{#define Debug_HearthBeat() (GPIO_PORTA3 ^= 0x08)}
\]