

Brandt Westing

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- OBJECTIVE** To learn and be actively engaged in the fields of computing, engineering, and science.
- EDUCATION** **University of Texas at Austin** GPA 3.2/4.0
B.S Electrical and Computer Engineering; Minor in Astronomy May 2008
- COURSE WORK** Microprocessor Programming, C Programming, Data Structures (C++), Circuit Theory, Optical/Laser Engineering, DSP Architecture/Communications, Biomedical Electronics, Quantum Mechanics, and Technical Communication.
- EXPERIENCE**
- 9/2007-Present **Texas Advanced Computing Center** Austin, TX
Research Assistant in the Scientific Visualization Group
- Currently creating complex visualizations of the first stars and galaxies using the Paraview visualization application.
 - Using the VTK graphics library to develop in-house remote visualization software.
- 8/2006–8/2007 **Advanced Micro Devices, Inc.** Austin, TX
Systems Software Design Co-op Engineer in Firmware
- Maintained and developed source code for low level CPU and chipset diagnostics to facilitate new silicon debugging.
 - Modified low level pattern generation software to exploit new chip features on the HyperTransport system bus for characterization and verification.
 - Developed and deployed a diagnostic utility for servers that required a custom configured and built kernel on a Linux Live CD with the BusyBox utility. Wrote documentation for this diagnostic.
 - Worked on adding support for a PCI memory device on pattern generation software.
 - Developed experience with software development life cycle using CVS, SVN, and test driven development.
- 6/2006 – 8/2006 **McDonald Observatory** Fort Davis, TX
Systems Engineer/Intern
- Researched a theoretical design for a telescope control system to be implemented on the Hobby-Eberly Telescope.
 - Created technical documentation for proposed design and presented specifications to observatory staff.
 - Presented work at the American Astronomical Society Conference, Seattle 2007.
- SKILLS**
- Software:** Linux, Microsoft-DOS, OSX, Darwin, Windows XP/Server, SVN, CVS, Matlab, LabVIEW, Microsoft Office.
Hardware: Digital Systems Design, Sequential Circuit Design, Solid State Electronics, Signal Generators, Oscilloscopes.
Languages: C, C++, Ruby, Java, Perl, HTML, CSS, VHDL, Freescale 6812 Assembly, TI TMS320C6000 VLIW DSP, x86 Assembly(familiar).
- HONORS AND ACTIVITIES**
- NSF grant: Research Experience for Undergraduates, 2006
Active member, Institute for Electrical and Electronics Engineers, 2006
Member UT Astronomy Club, American Astronomical Society 2007