Homework 2 Due: Tuesday 7/1 @ start of lab 2:00pm

1) Reading Assignment - Read Chapter 5 and Chapter 7 sections 4-7 in the E-Book
http://users.ece.utexas.edu/~valvano/Volume1/E-Book/

2. Task to turn in: Copy and run the code in Design Example (7.7) of E-Book. Specifically, here are the tasks to perform:
   a. Create a copy of your Lab1 project called HW2. See the video here on how to create a copy of an assembly project with a different name: https://www.youtube.com/watch?v=l6IzNHVjohU
      This video shows how to do this for an assembly project, the process is identical for a C project, only you are dealing with .c files instead of .s files.
   b. Build the project and show the running of the project with this scenario:
      I. Run in Simulation: Use PortF peripheral simulation
      II. Monitor PF3 on the Logic Analyzer
      III. SW1 is pressed
      IV. Take a screenshot of the Logic Analyzer with 1 min worth of activity.
   c. How many SOS sequences are in 1 min of Logic Analyzer capture with SW1 pressed? Explain why there are that many.