

EE338L
Homework 3
Due Tu 3/6/07

Use the following transistor parameters:
NFET: $K_p=150\mu\text{A}/\text{V}^2$, $V_t=0.7\text{V}$, $\lambda=0.03$
PFET: $K_p=50\mu\text{A}/\text{V}^2$, $V_t=0.8\text{V}$, $\lambda=0.03$

Problem 1:
Calculate the operating point for the two circuits.
Then calculate the small signal gain.

Problem 2:
Given a $10\mu\text{A}$ current source, design a current sink that can sink $100\mu\text{A}$, has output impedance of the order of $g_m \cdot r_o^2$ and works down to $V_{out}=0.4\text{V}$.
Then design a current source that can source $100\mu\text{A}$, has output impedance of the order of $g_m \cdot r_o^2$ and works up to $V_{out} = V_{DD}-0.4\text{V}$.

Problem 3:
Start with a $10\mu\text{A}$ current source. Design a differential pair such that the transconductance $G=\Delta I_{out}/V_{dm}=1\text{mS}$.
 V_{cm} can range from 1V to 1.5V . Make sure that the all devices are in saturation for $v_{cm}=1\text{V}$. What is the minimum v_{out} for $v_{cm}=1.5\text{V}$

Problem 4: calculate the currents I_1, I_2, I_3, I_4, I_5 assuming that all devices are in saturation. Exceptions may be M_1 and M_2 – decide and justify.
All devices are identical, the multiplier M indicates how many unit devices are hooked up in parallel.

