EE338L Spring 2007 Lab 2,
due 2/22

2.1 Use the simulator to verify problem 1 in the homework
Plot resistance vs input voltage (DC analysis)
Plot settling time in response to a step (transient analysis)

2.2 Use the simulator to verify problem 2 in the homework
Plot Vout vs Vin (DC analysis)
Set up the proper operating point and plot gain vs frequency (AC analysis)
Plot the noise vs. frequency for the operating point – input referred and output
referred (Noise analysis is part of AC analysis)