The University of Texas at Austin
Dept. of Electrical and Computer Engineering
Midterm #1 Version 2.0

Date: March 8, 2023 Course: EE 445S Evans

Name: ____
Last, First

- **Exam duration**. The exam is scheduled to last 75 minutes.
- **Materials allowed**. You may use books, notes, your laptop/tablet, and a calculator.
- **Disable all networks**. Please disable all network connections on all computer systems. You may not access the Internet or other networks during the exam.
- **Electronics.** Power down phones. No headphones. Mute your computer systems.
- **Fully justify your answers**. When justifying your answers, reference your source and page number as well as quote the particular content in the source for your justification. You could reference homework solutions, test solutions, etc.
- **Matlab**. No question on the test requires you to write or interpret Matlab code. If you base an answer on Matlab code, then please provide the code as part of the justification.
- **Put all work on the test**. All work should be performed on the quiz itself. If more space is needed, then use the backs of the pages.
- **Academic integrity.** By submitting this exam, you affirm that you have not received help directly or indirectly on this test from another human except the proctor for the test, and that you did not provide help, directly or indirectly, to another student taking this exam.

Problem	*Point Value*	*Your score*	*Topic*
1	25		IIR Filter Analysis
2	24		Increasing the Sampling Rate
3	27		Equalizer Design
4	24		Potpourri
Total	104		

Problem 1.1 *IIR Filter Analysis*. 25 points.

Consider the following causal linear time-invariant (LTI) discrete-time infinite impulse response (IIR) filter with input $x[n]$ and output $y[n]$ described by

$$y[n] - b^2\, y[n-2] = x[n]$$

for $n \geq 0$, where b is a real-valued positive coefficient less than one, i.e. $0 < b < 1$.

Please note that the coefficient in front of the $y[n-1]$ term is zero.

(a) What are the initial conditions and their values? Why? *6 points*.

(b) Draw the block diagram of the filter relating input $x[n]$ and output $y[n]$. *6 points*.

(c) Derive a formula for the transfer function in the z-domain and the region of convergence. *4 points*.

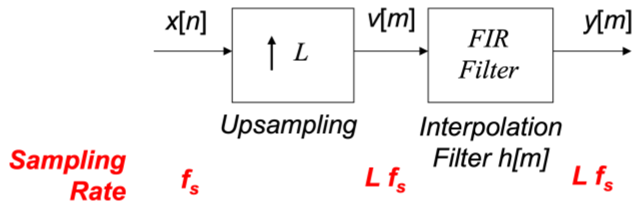
(d) Give a formula for the discrete-time frequency response of the filter. *3 points*.

(e) What frequency responses are possible among lowpass, highpass, bandpass, bandstop, allpass and notch? For each possibility, give a value of b that would give that response. *6 points*.

Problem 1.2 *Increasing the Sampling Rate*. 24 points.

Upsampling by L can be used to increase the sampling rate of the input signal by a factor of L.

A lowpass finite impulse response (FIR) filter can then be applied to the output of the upsampler to attenuate the high frequencies introduced by upsampling.



On the right, discrete-time index n is associated with sampling rate f_s and discrete-time index m is associated with sampling rate $L\ f_s$.

(a) What is the maximum continuous-time frequency f_{max} that is present in $x[n]$? What discrete-time frequency does f_{max} correspond to? *6 points*.

(b) What discrete-time frequency in $v[m]$ corresponds to the maximum continuous-time frequency f_{max} that is present in $x[n]$? *6 points*.

(c) Any discrete-time frequencies present in $v[m]$ higher than your answer in part (b) but less than π rad/sample correspond to frequencies introduced by upsampling. Give the discrete-time passband frequency ω_{pass} and stopband frequency ω_{stop} you would use for the lowpass filter design. *6 points*.

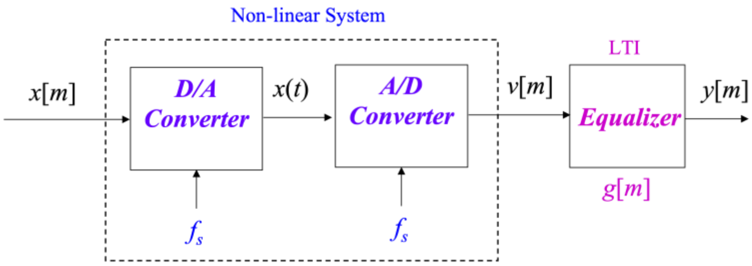
(d) Give two ways to design a lowpass FIR filter with linear phase to meet the specifications of a discrete-time passband frequency ω_{pass} and stopband frequency ω_{stop}. A "way" to design the filter could be a formula, algorithm, etc. *6 points*.

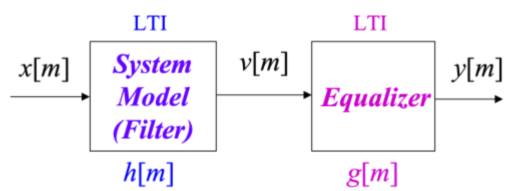
Problem 1.3 *Equalizer Design.* 27 points.

Many applications use a digital-to-analog (D/A) converter and an analog-to-digital (A/D) converter.

- An audio system would use a D/A converter for playback over earbuds or speakers and an A/D converter in a microphone for recording and mixing.
- A digital communication system would use a D/A converter in the transmitter and an A/D converter in the receiver.

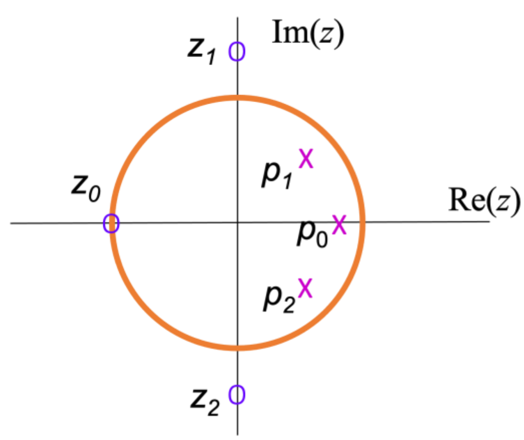
You'll design a linear time-invariant (LTI) bound-input bounded-output (BIBO) stable discrete-time equalizer to compensate the distortion in the nonlinear system (on the left) using an LTI model of the nonlinear system (on right):





(a) Give a formula for a test signal to use for $x[m]$ that would be of finite duration. The test signal should have all discrete-time frequencies in it. The test signal would be used to estimate the impulse response $h[m]$ for the LTI model of the cascade of the D/A and A/D converters. *12 points*.

(b) Given the poles and zeros of $H(z)$ below, give the values for the poles and zeros of $G(z)$ and draw them on the pole-zero diagram on the right for an LTI equalizer $G(z)$ that would make the cascade of the LTI system model $H(z)$ and the LTI equalizer $G(z)$ be allpass. *15 points*.



$$p_0 = 0.9$$
$$p_1 = 0.9\, e^{j\frac{\pi}{4}}$$
$$p_2 = 0.9\, e^{-j\frac{\pi}{4}}$$

$$z_0 = -1$$
$$z_1 = 1.25\, e^{j\frac{\pi}{2}}$$
$$z_2 = 1.25\, e^{-j\frac{\pi}{2}}$$

Problem 1.4. *Potpourri*. 24 points.

(a) An integrator is a common building block in systems. The discrete-time version of the integrator is the running summation, which is defined for input $x[n]$ and output $y[n]$ for $n \geq 0$ as follows:

$$y[n] = \sum_{m=0}^{n} x[m]$$

The summation requires unbounded memory as $n \to \infty$. A more efficient implementation is

$$y[n] = y[n-1] + x[n] \text{ for } n \geq 0$$

I. Give the initial condition(s) for the more efficient implementation to be linear and time-invariant (LTI). *4 points*.

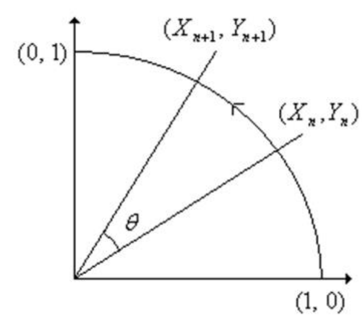
II. Give a formula for and plot the impulse response $h[n]$ of an LTI running summation. *4 points*

III. The running summation only gives an unbounded output for a bounded input when the input has a non-zero constant component. What filter would you apply before the running summation to prevent the running summation being bounded-input bounded-output unstable? *4 points*

(b) There are several algorithms [1] to generate a cosine signal $x[n] = \cos(\omega_0 n)$ and a sine signal $y[n] = \sin(\omega_0 n)$ at the same time using rotation. For each signal, the argument is $0, \omega_0, 2\omega_0, \ldots,$ for $n = 0, 1, 2, \ldots$

Using the visual representation on the right, the next cosine value X_{n+1} and next sine value Y_{n+1} are computed from the current cosine value X_n and current sine value Y_n using a rotation operation:

$$\begin{bmatrix} X_{n+1} \\ Y_{n+1} \end{bmatrix} = \begin{bmatrix} \cos(\theta) & -\sin(\theta) \\ \sin(\theta) & \cos(\theta) \end{bmatrix} \begin{bmatrix} X_n \\ Y_n \end{bmatrix}$$



Matrix Rotation [1]

Using rotation keeps the values of the cosine and sine for the same angle on the unit circle.

With $\theta = \omega_0$, we use the rotation approach starting at $n = 0$ and pre-compute $\cos(\theta)$ and $\sin(\theta)$.

I. What are the values of X_0 and Y_0? *4 points*.

II. How many multiplications per output sample are needed to compute the cosine and sine signals? *4 points.*

III. Compare your answer in part II to using second-order difference equations to compute cosine and sine signals separately. *4 points.*

[1] Juhan Nam, "A Study of Sinusoid Generation Using Recursive Algorithms", Stanford University, 2005.