

SENIOR LAB

- EE 464K
regular senior lab
about $2/3$ students
- EE 464H
honors senior lab
about $1/3$ students
- EE 464I
industrial senior lab
new; no I in catalog
1 or 2 students/semester so far

CONTENT

- Projects must contain substantial design (ABET).
- Projects taken from concept to completion.
- Lots technical communication (oral reports, written reports, lab notebook, etc.).
- 1 project per group of 2 students (recently changed from long history 2 projects 464K).
- 464K projects assigned in analog circuits, communications, control/robotics, digital systems, DSP, microwave/acoustics, optics, and power. Tend to be hardware based; all have or will shortly have a software component.

Honors (464 H)

- Require 3.5 GPA (3.25-3.5 K doing H)
- Students set up project with a faculty supervisor or sponsor.
- Any group size allowed (2 most common)
- About 50% primarily hardware and 50% primarily software projects this semester (25 projects)
- Group has supervising TA and does same technical communication as K.

Industrial (I)

- This option is for students who work full time and are part time students (not for coop students or students who work part time).
- Must have a suitable design project that the employer is willing to support.
- Student must have both a work supervisor and a faculty member to oversee the project.
- Student must have permission to present the full scope of the project in the oral and written reports.

2/29/00 Comments/Suggestions

- K labs are too long.
- Consider ME senior lab format.
- Involve more faculty (it was pointed out that we devote more faculty at beginning of curriculum (302) than at end (464)).
- Go to honors (H) format for all (H & K) senior lab. Have K students do a project agreement in prior semester, in their interest area. Make course 3 cr hour plus 1 cr hour in prior semester to fully define project.