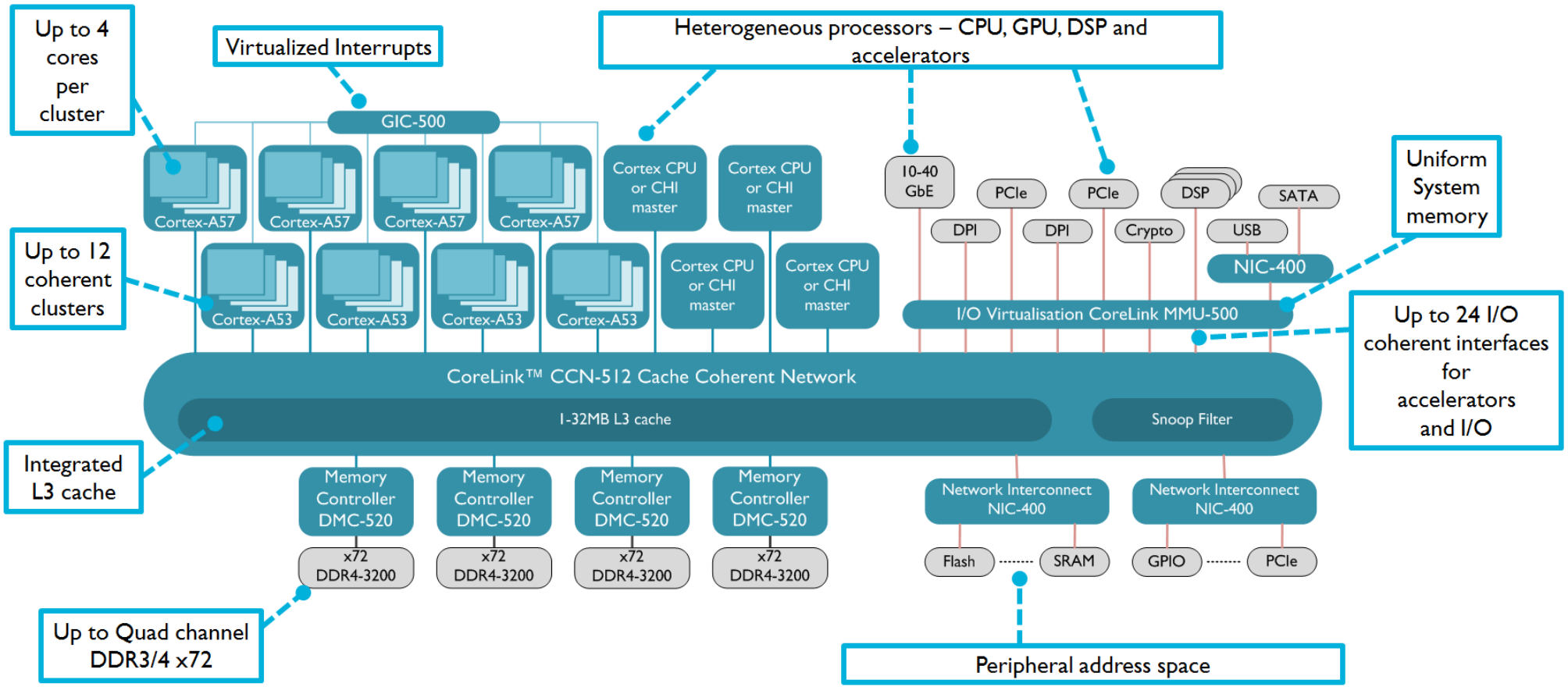
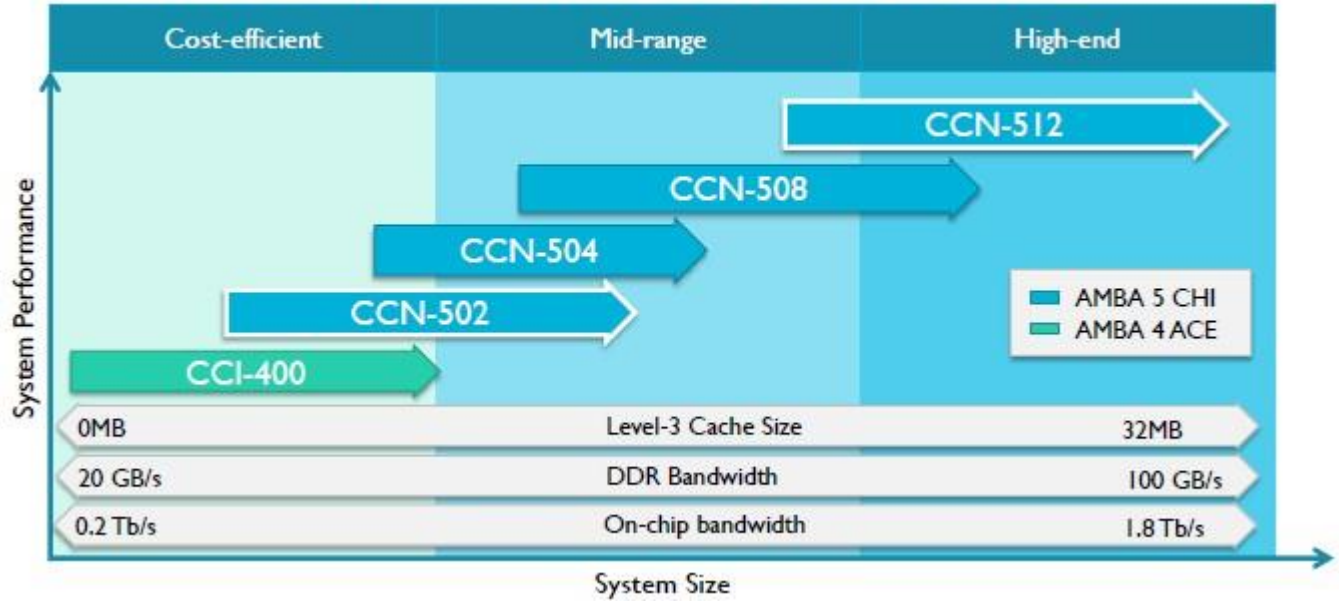
ARM CoreLink (CCN-502 and CCN-512)

- Scalable interconnect for SoC allowing up to 16/48 cores
- Supports up to 4/12 clusters with 4 cores per cluster
- Designed to be used with 64-bit Cortex-A54 and Cortex-A57 cores
- Increased processing capability of 512 is targeted at x86-dominated datacenters
- 502 takes 70% area or 504, yet still delivers similar performance. Aimed at low-end, low-power solutions

CoreLink™ CCN-512 and DMC-520



Scalable Efficient Interconnect for Compelling Solutions



	CoreLink™ CCN-502	CoreLink™ CCN-512
Key Benefits	Area Optimized, up to 70% saving on CCN-504 at 1MB	Dense Compute, scaling up to 48 cores
Processor Clusters	1 to 4 Clusters including Cortex-A53 and Cortex-A57	Up to 12 Clusters including Cortex-A53 and Cortex-A57
Performance	Up to 0.8 Tbps	Up to 1.8 Tbps
DDR3/4	1 to 4 channels with DMC-520	1 to 4 channels with DMC-520
IO	9x AXI4/ACE-Lite	24x AXI4/ACE-Lite
Level 3 Cache	0 to 8 MB	1 to 32 MB

References

- http://www.arm.com/products/system-ip/interconnect/
- http://www.enterprisetech.com/2014/10/22/arm-chip-interconnect-spans-lot/
- http://www.apm.com/products/data-center/x-gene-family/x-gene/
- http://semiaccurate.com/2014/10/22/arm-updates-corelink-ccn-502-ccn-512/