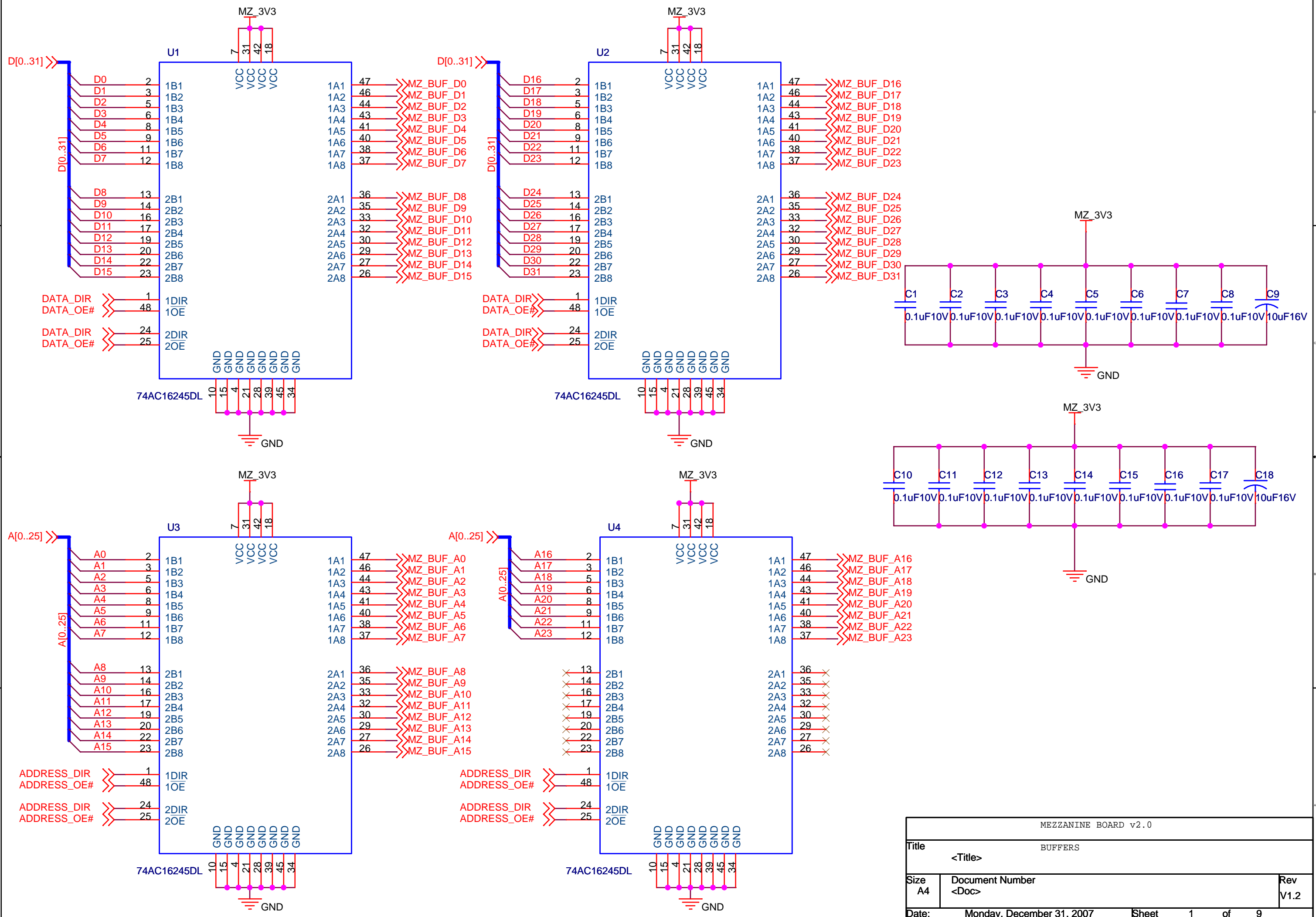
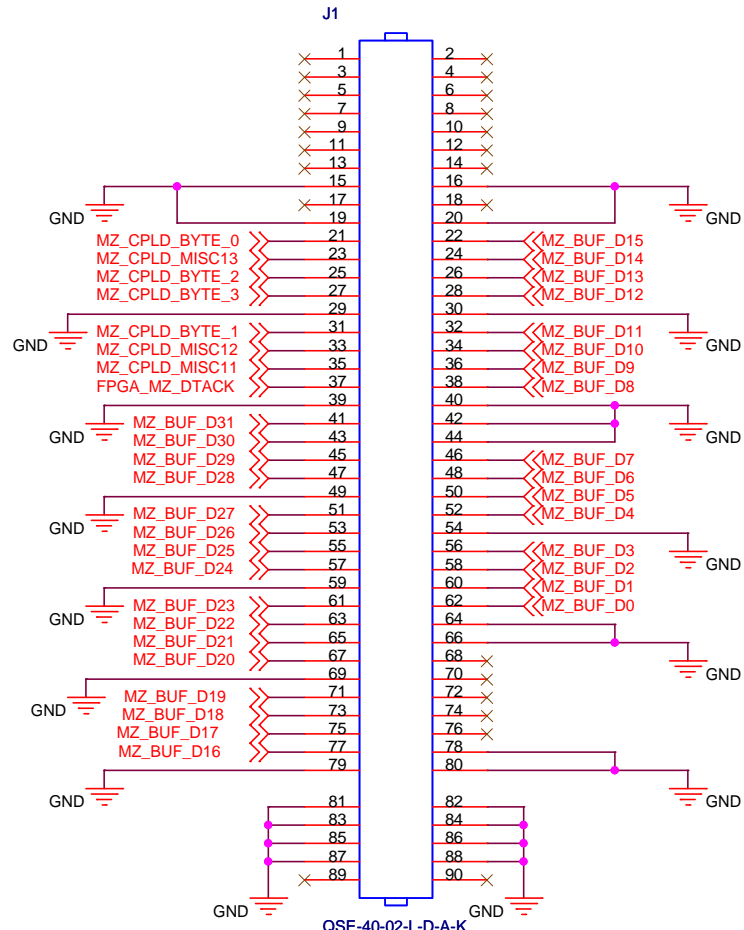


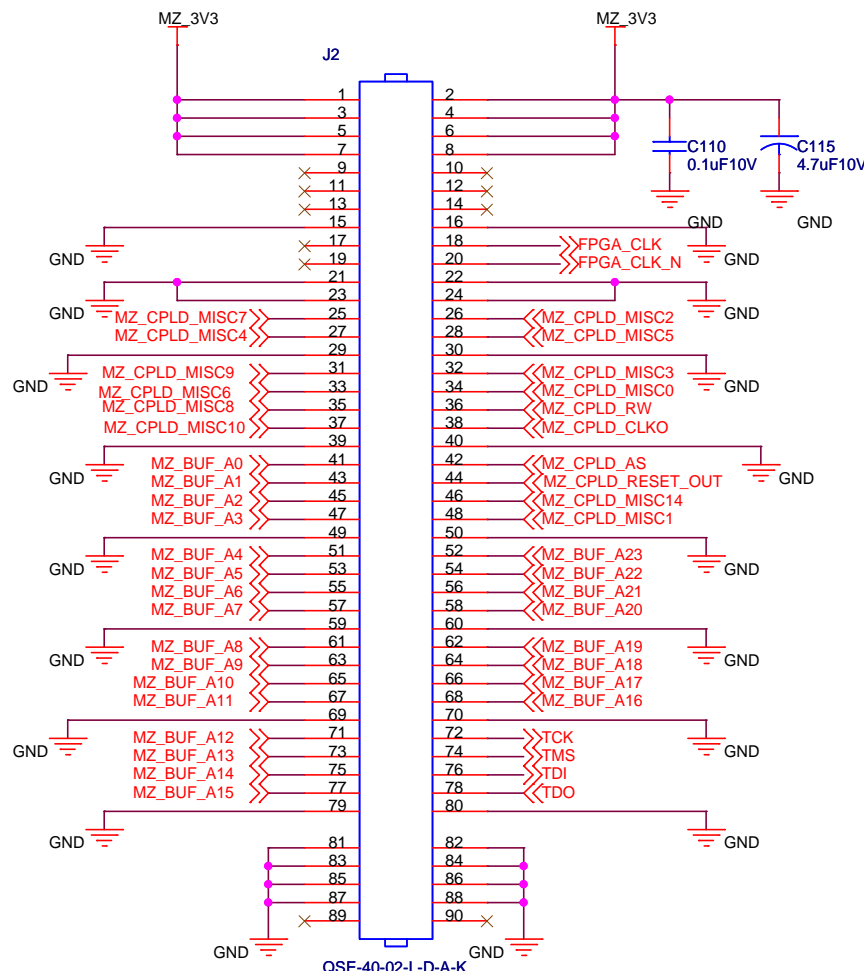
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Size	Document Number	Rev
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Date:	Monday, December 31, 2007	Sheet 0 of 0



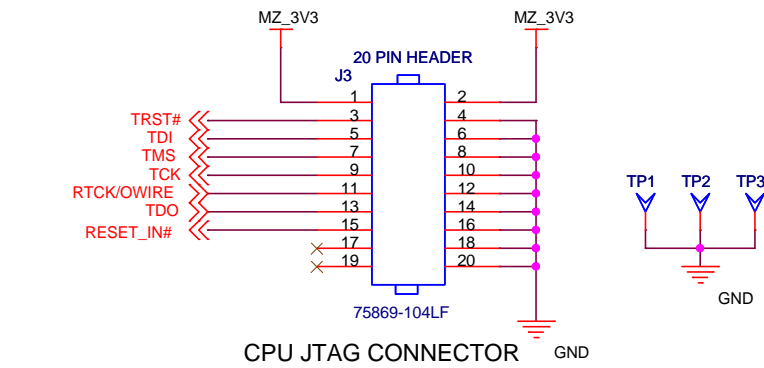
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Size	Document Number	Rev
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Date:	Monday, December 31, 2007	Sheet 1 of 9



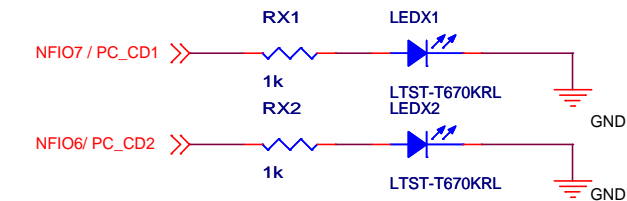
MEZZANINE CONNECTOR I



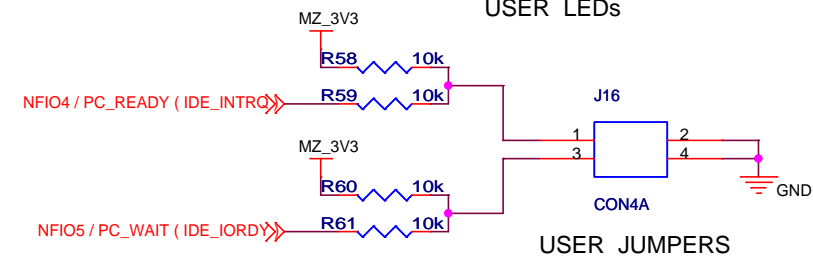
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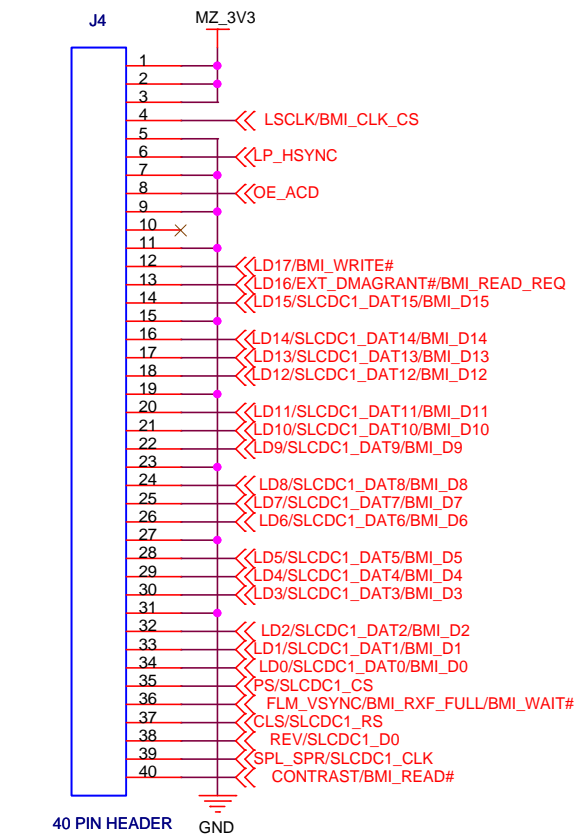
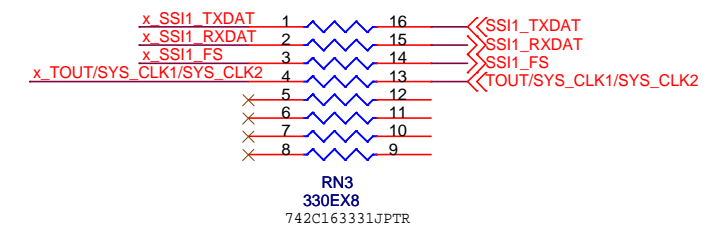
CPU JTAG CONNECTOR



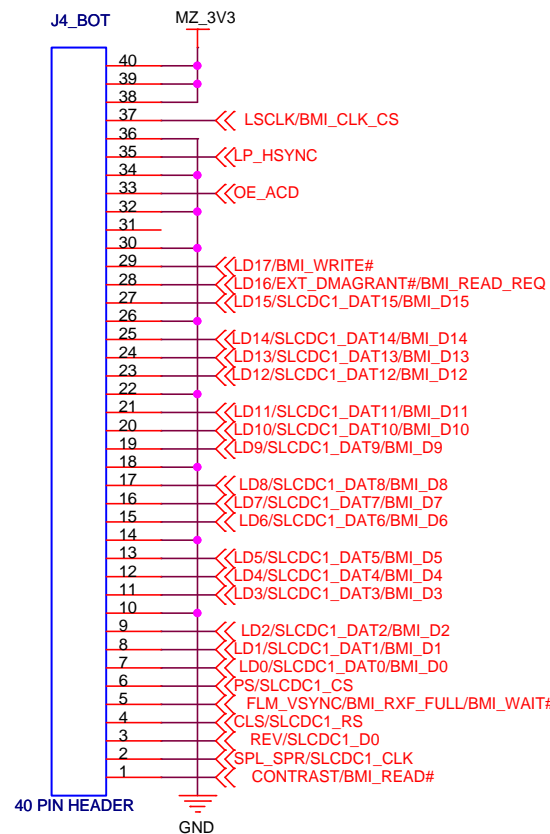
USER LEDs



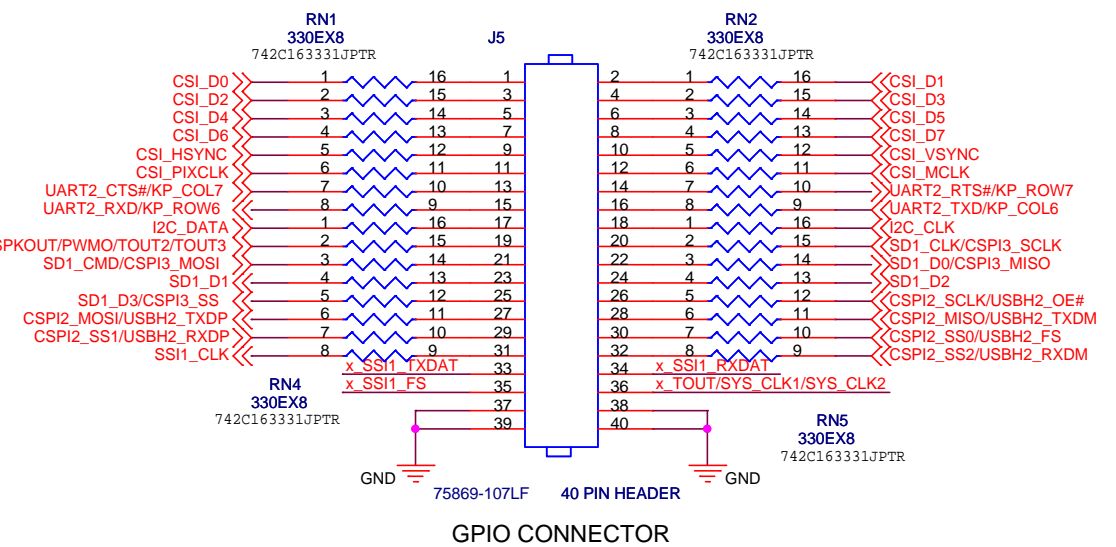
USER JUMPERS



LCD CONNECTOR_TOP

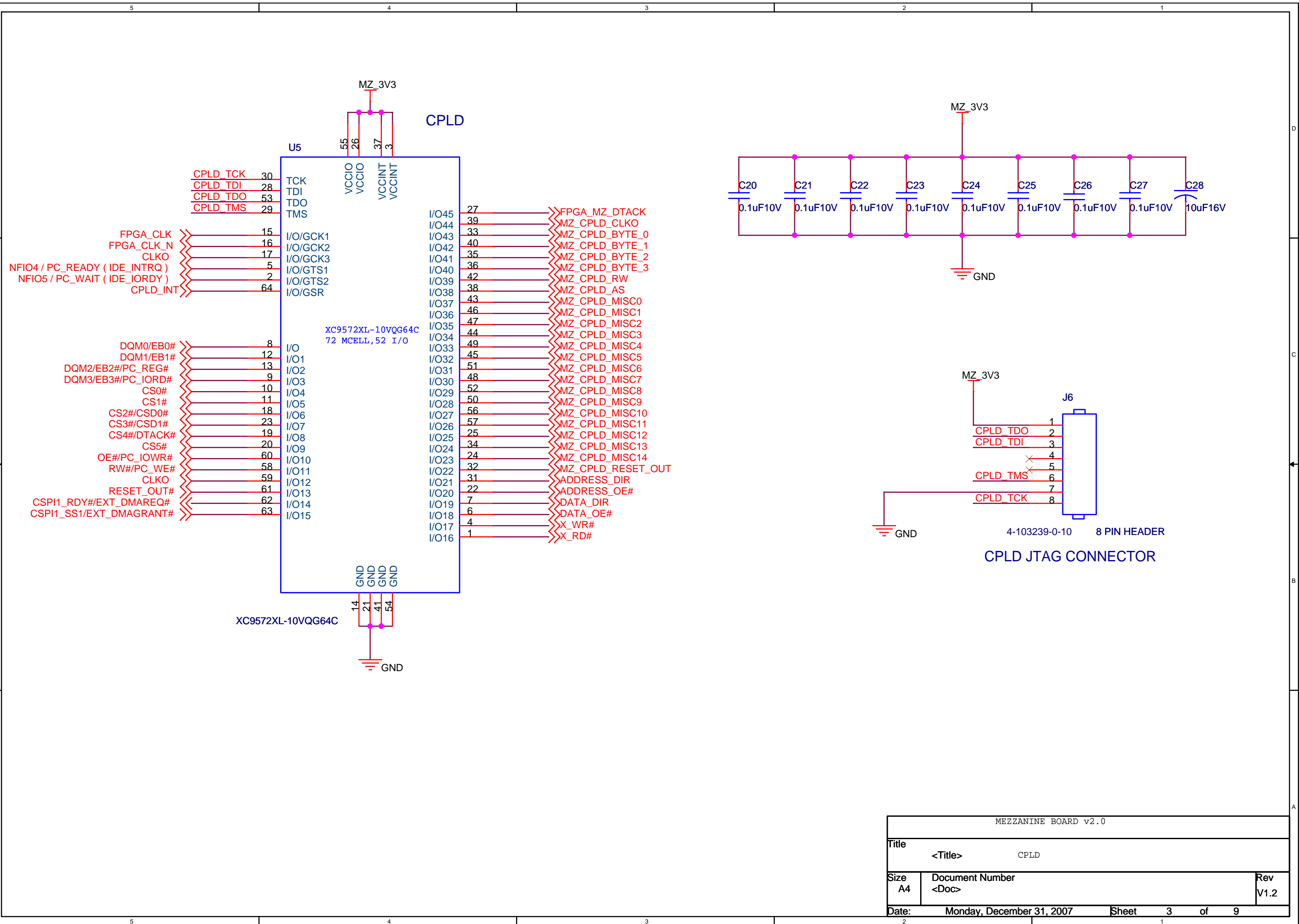


LCD CONNECTOR_BOT

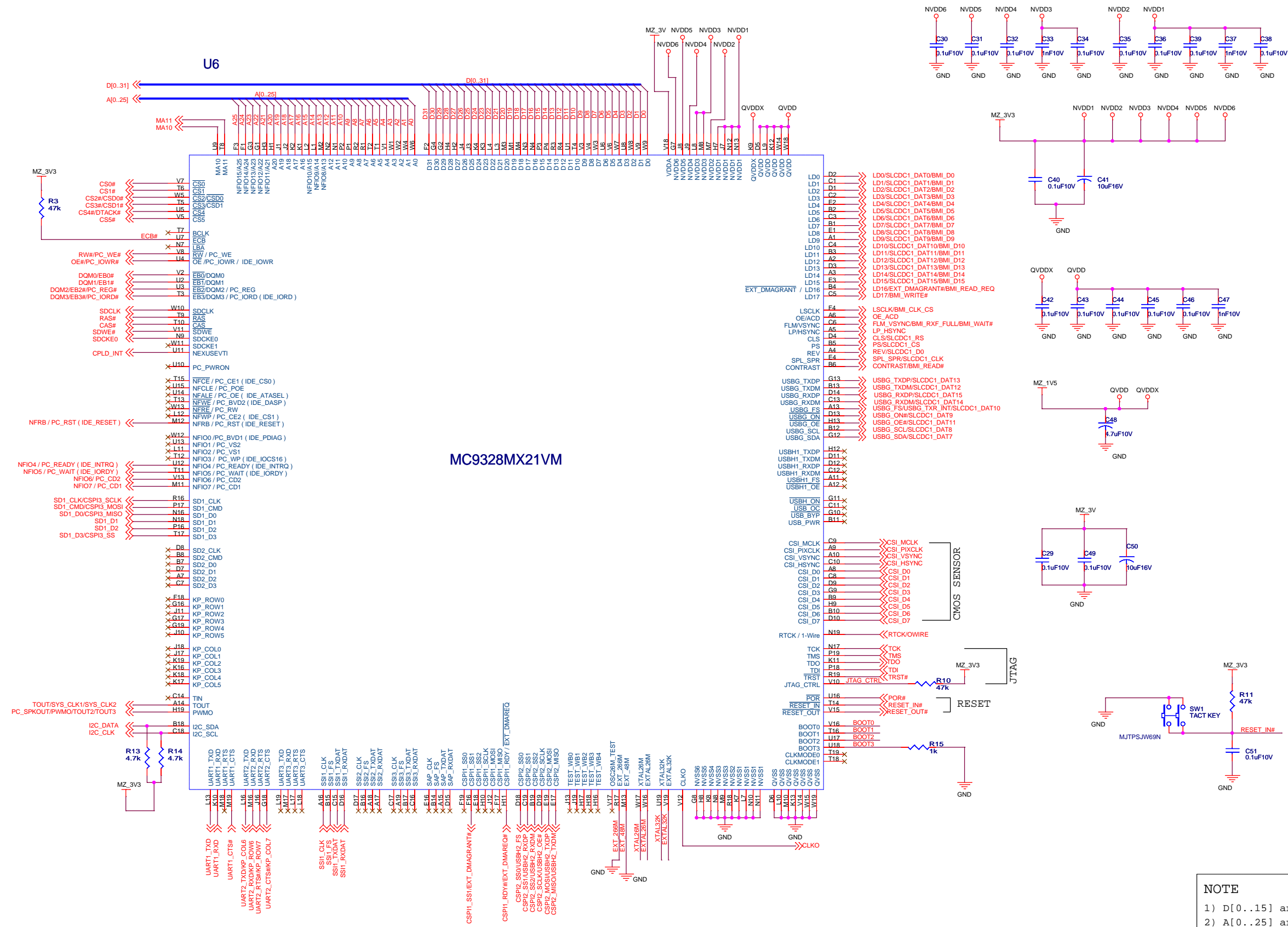


GPIO CONNECTOR

MEZZANINE BOARD v2.0		
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Date	Monday, December 31, 2007	Rev V1.2
	Sheet 2	of 9

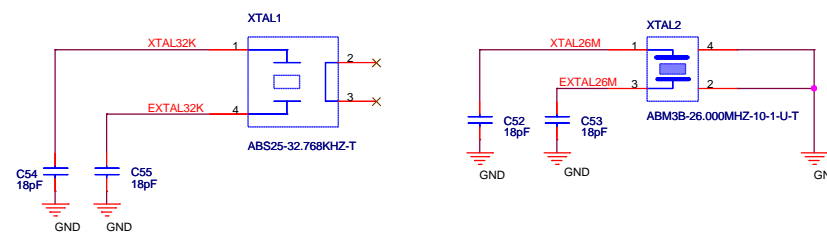


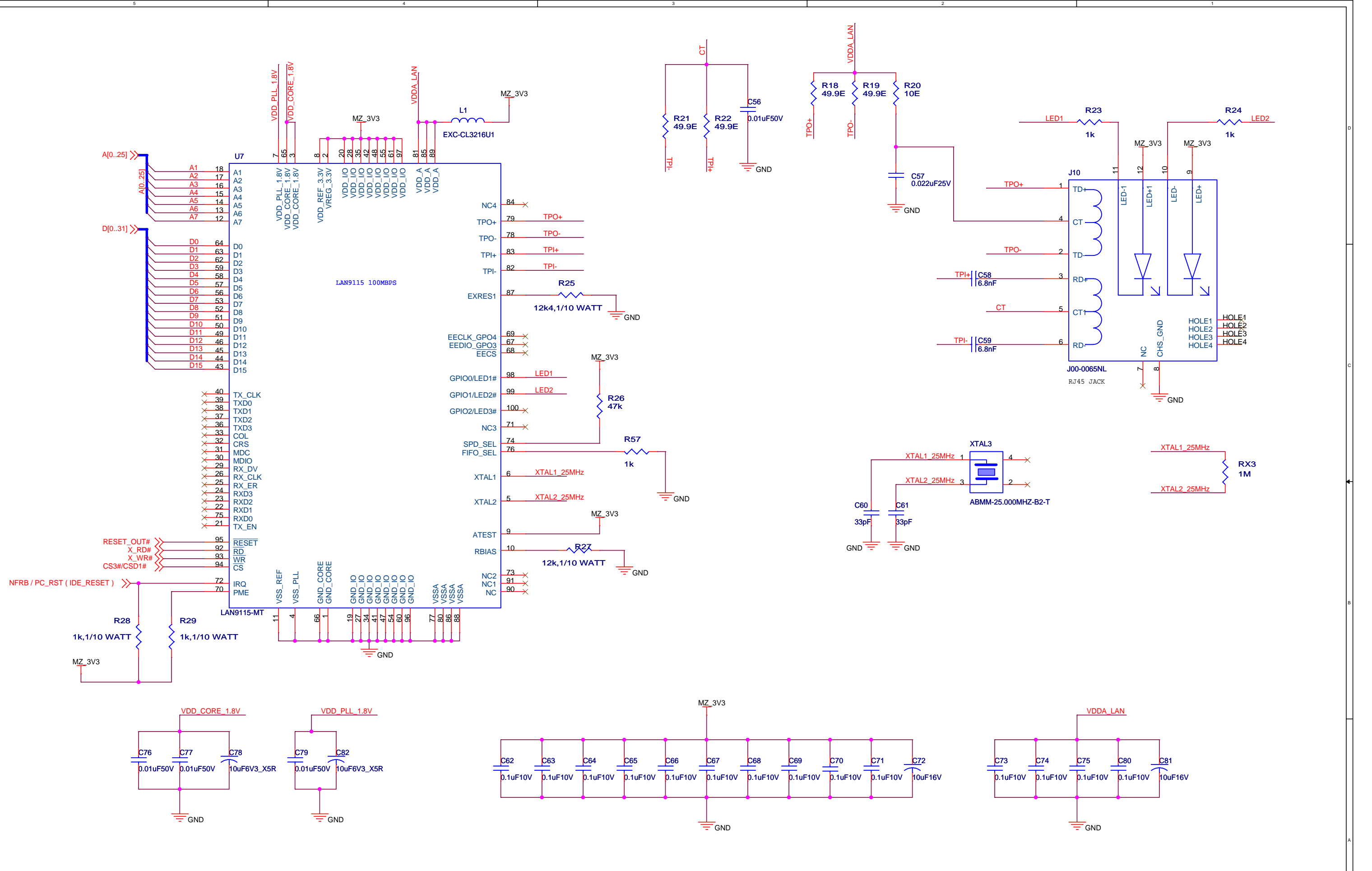
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Date:	Monday, December 31, 2007	Sheet 3 of 9



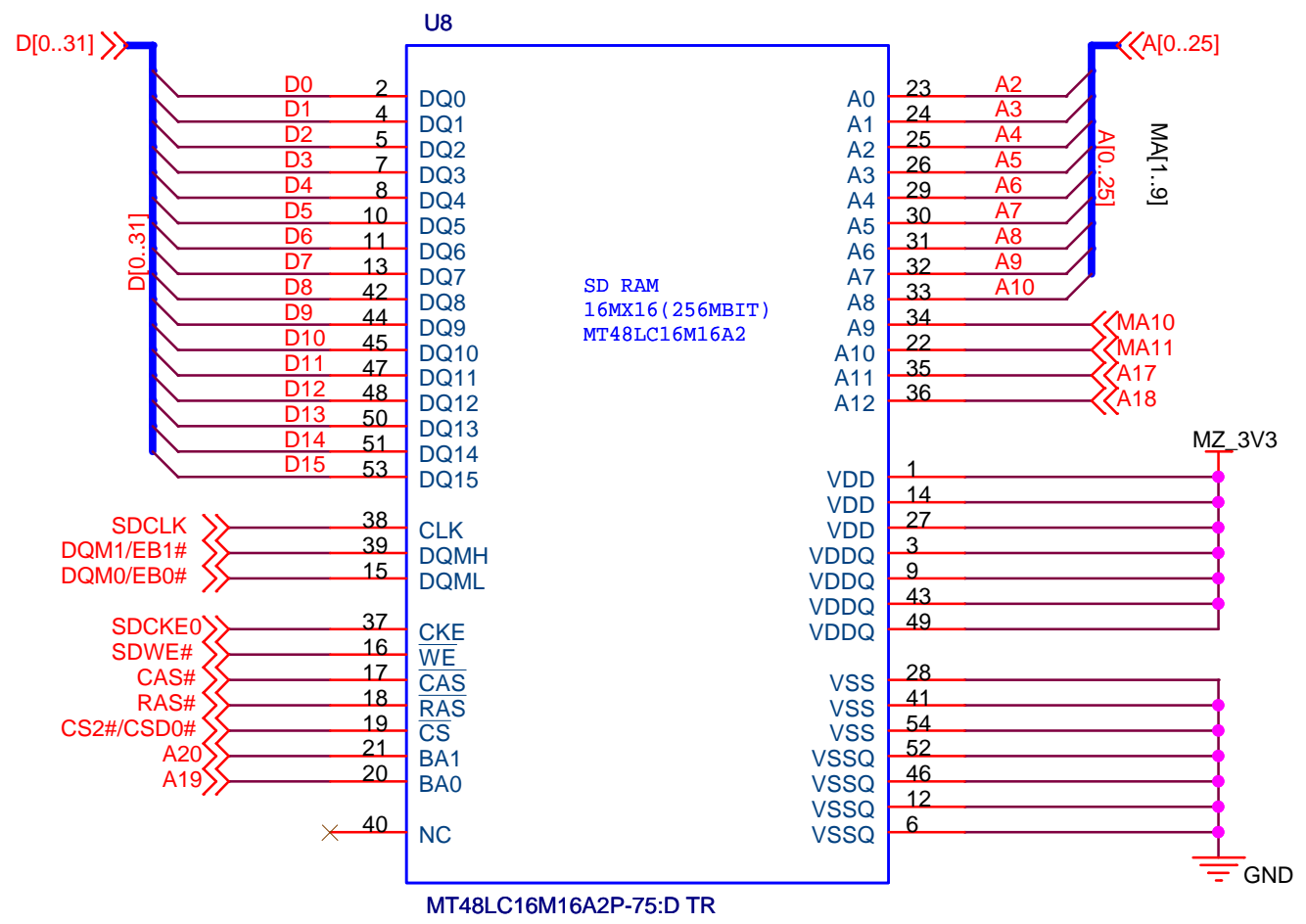
NOTE

- 1) D[0..15] are multiplexed with PC_D[0..15]
- 2) A[0..25] are multiplexed with PC_A[0..25]
- 3) A[1..10] are multiplexed with MA[0..9]
- 4) A[13..15] are multiplexed with NF_IO[8..10]
- 5) A[16..20] are multiplexed with SDBA[0..4]
- 6) A[21..25] are multiplexed with NF_IO[11..15]
- 7) A[21..24] are multiplexed with SDBIA[0..3]

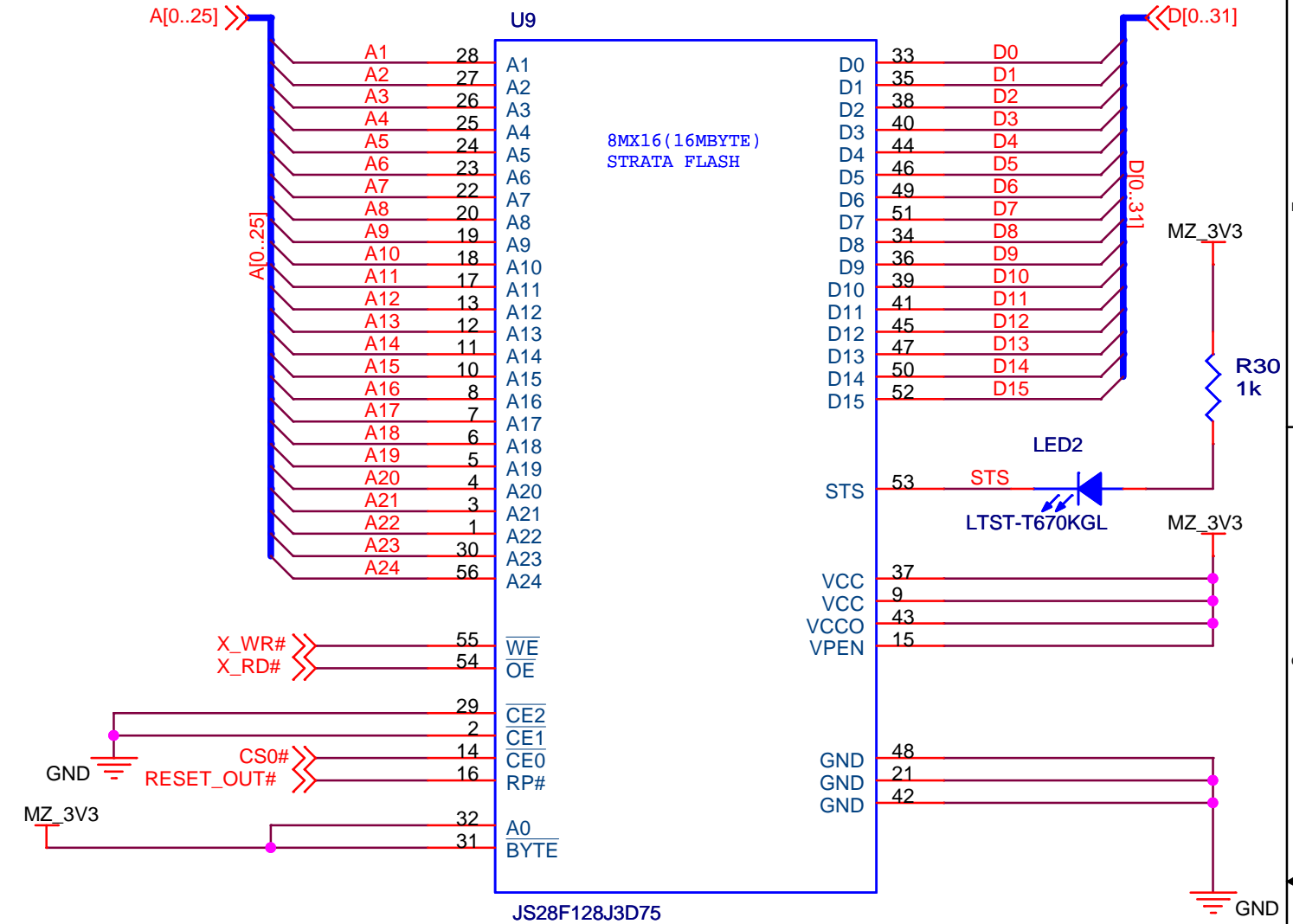




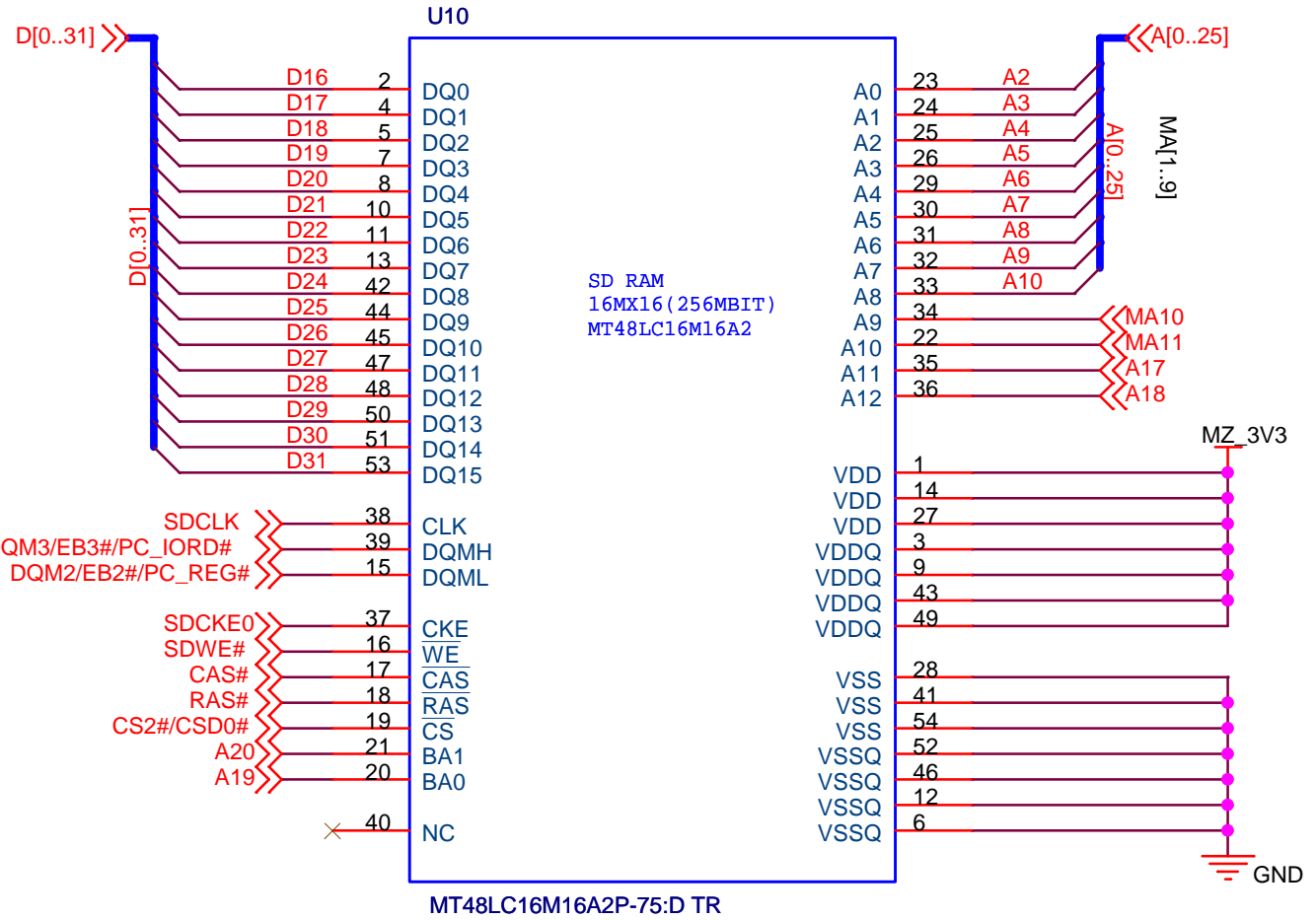
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Date:	Monday, December 31, 2007	Sheet 5 of 9



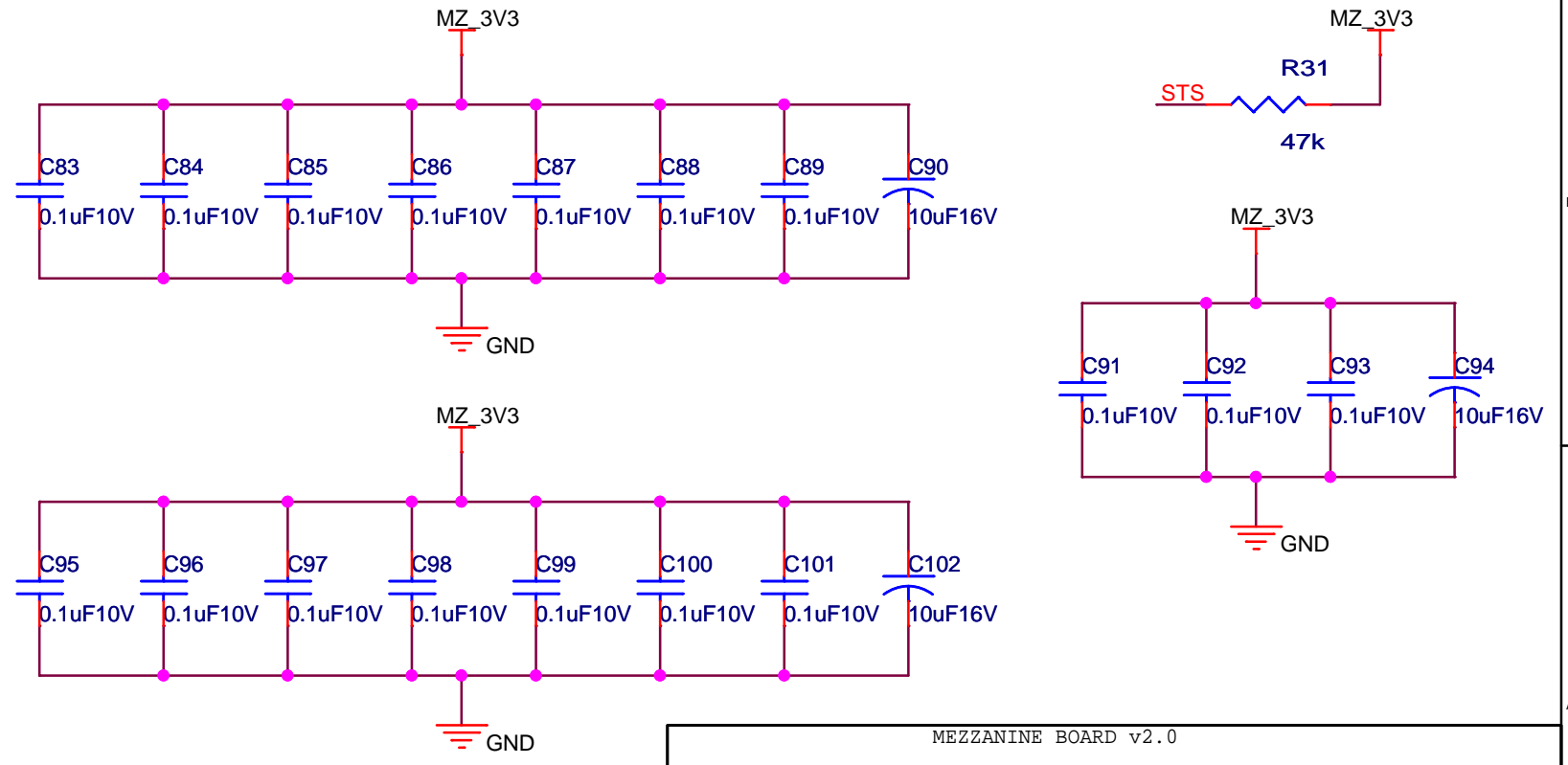
MT48LC16M16A2P-75:D TR



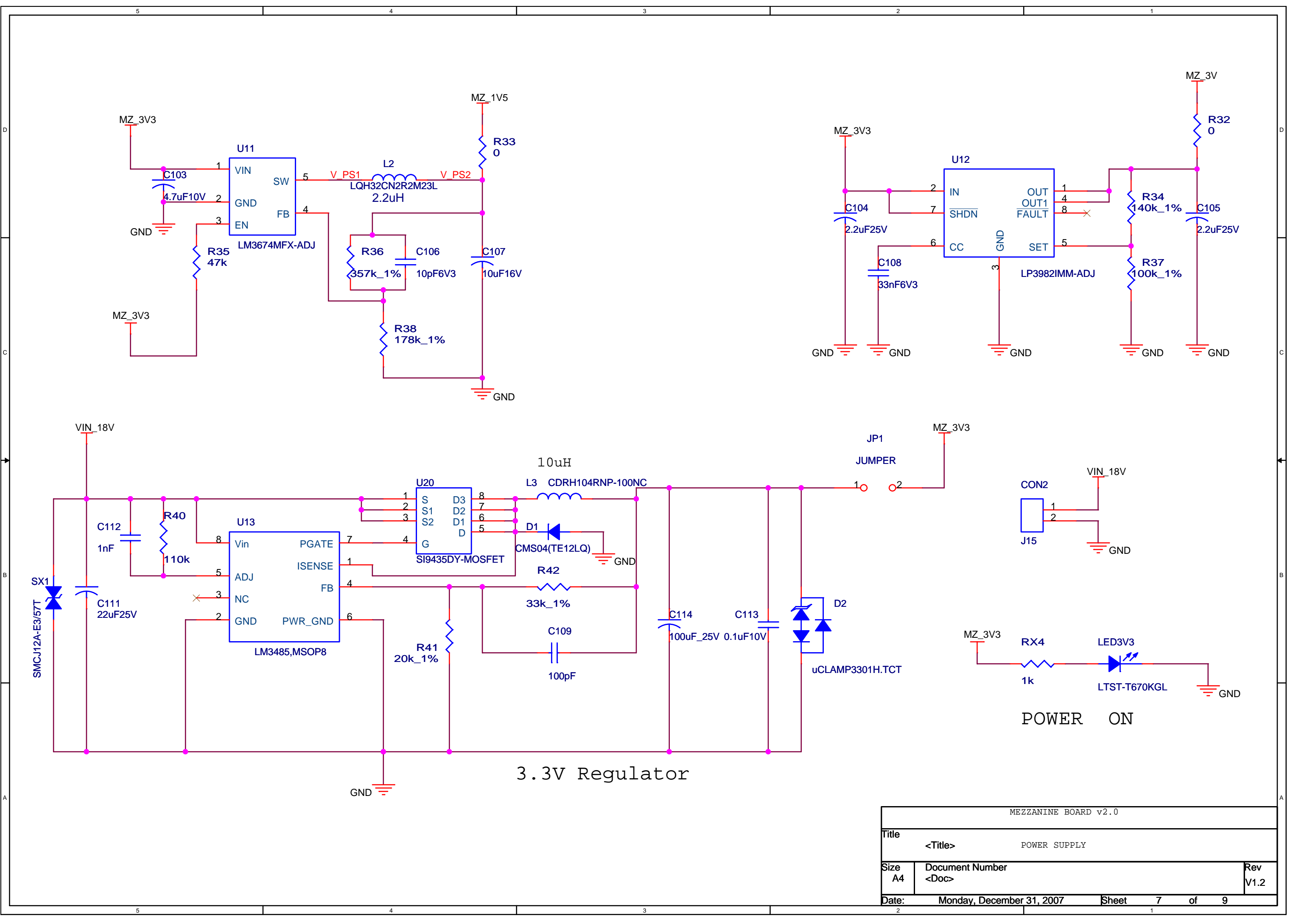
JS28F128J3D75



MT48LC16M16A2P-75:D TR



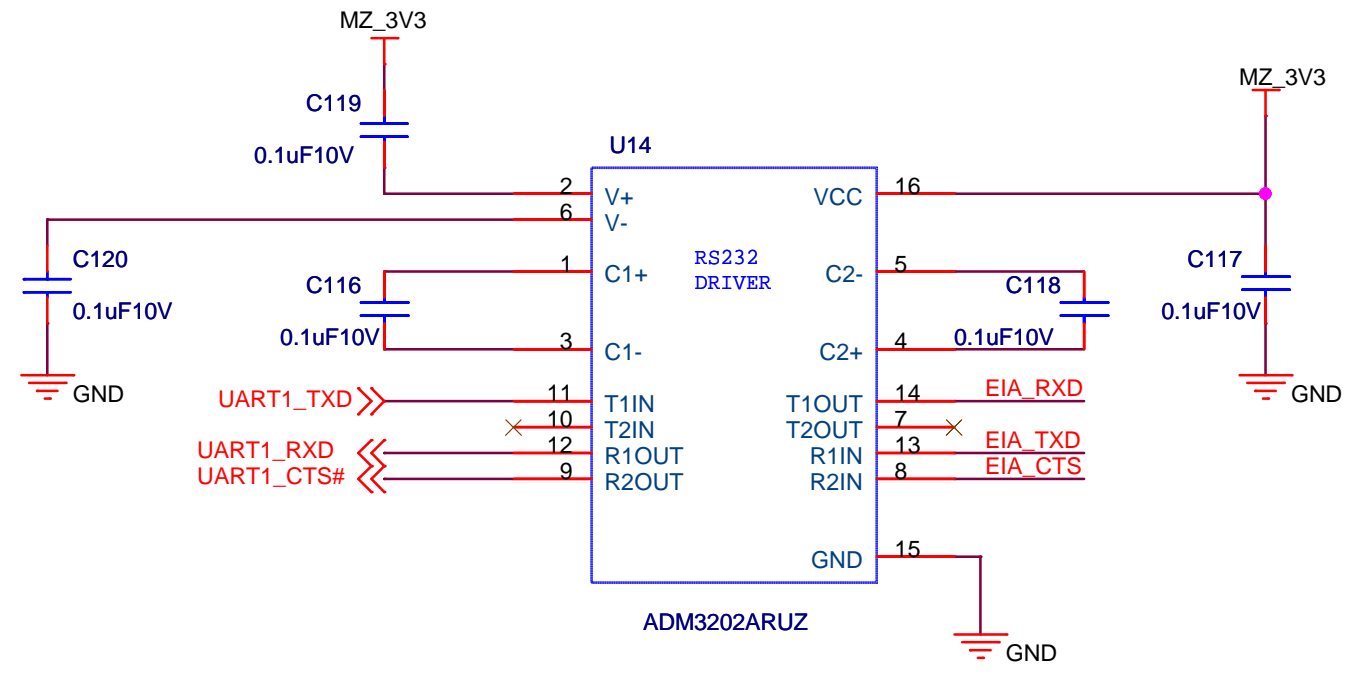
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Date:	Monday, December 31, 2007	Sheet 6 of 9



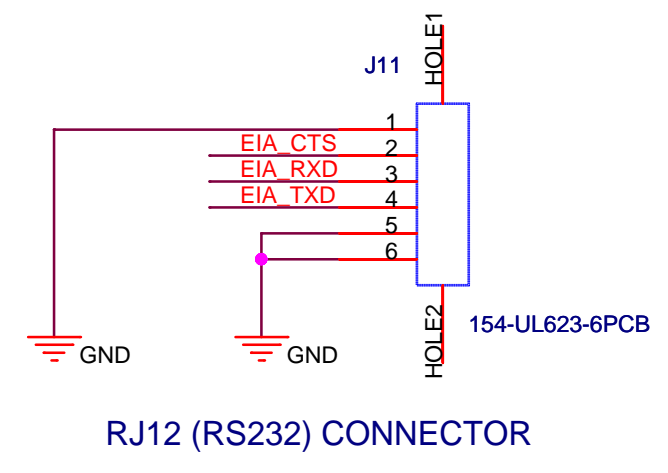
3.3V Regulator

POWER ON

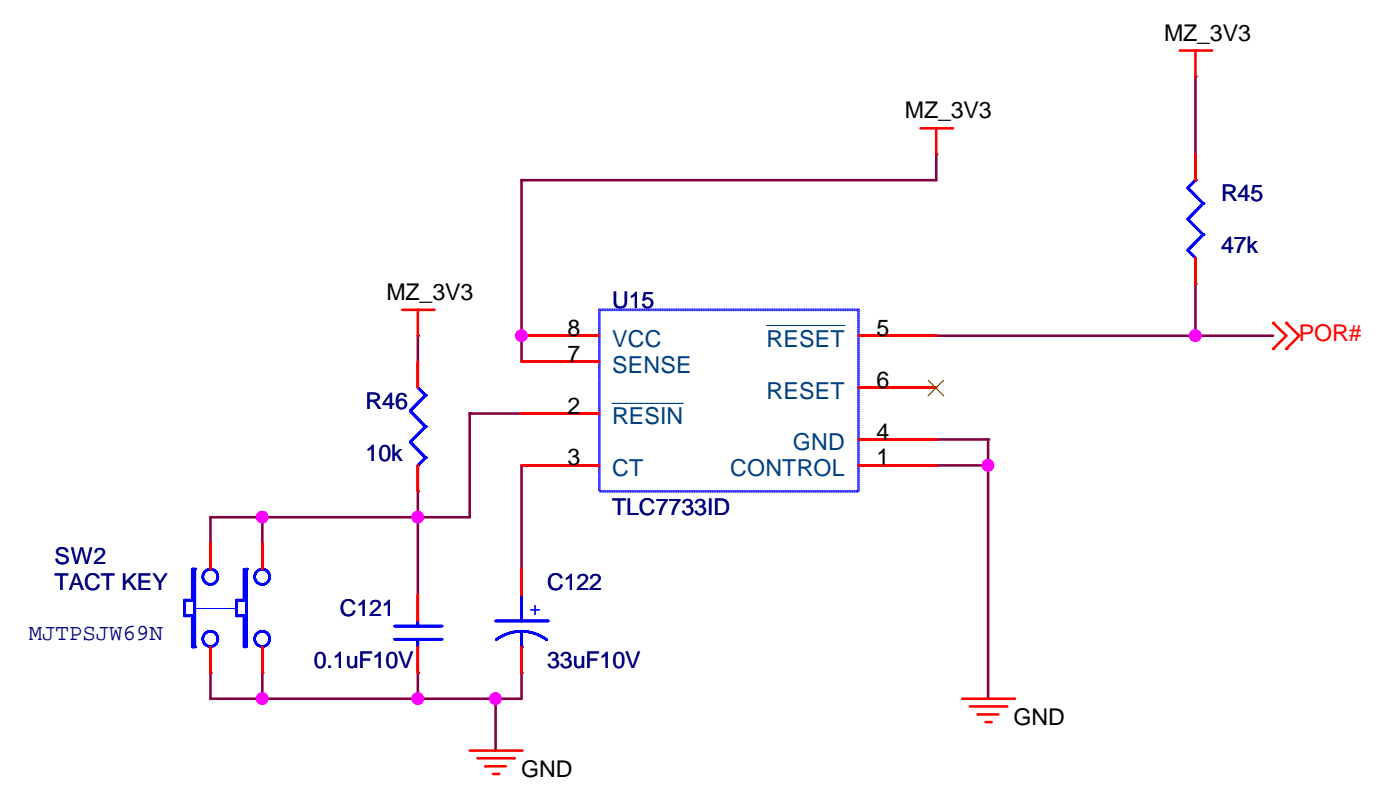
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Date:	Monday, December 31, 2007	Sheet 7 of 9



RS232 TO TTL CONVERTER



RJ12 (RS232) CONNECTOR



RESET GENERATOR

MEZZANINE BOARD v2.0		
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Size	Document Number	Rev
A4	<Doc>	V1.2
Date:	Monday, December 31, 2007	Sheet 8 of 9

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D

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Title		
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Date:	Monday, December 31, 2007	Sheet 10 of 10

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