BEN	R	State
0	1	1
1	X	35
X	0	26

IR<11>	State
0	32
1	31

IR<11>	State
0	34
1	33

R	State
0	2
1	3

R	State
0	21
1	22

R	State
0	23
1	24

R	State
0	27
1	28

R	State
0	29
1	30

Figure 1: Constraint Tables

		COND1	COND0
$COND_0$	(Unconditional)	0	0
$COND_1$	(Ready (Memory, I/O))	0	1
$COND_2$	(Branch)	1	0
$COND_3$	(Link)	1	1

$$Ready = \overline{COND1} * COND0 * R$$

$$Branch = COND1 * \overline{COND0} * BEN$$

$$Link = COND1 * COND0 * IR<11>$$

Figure 2: COND Logic

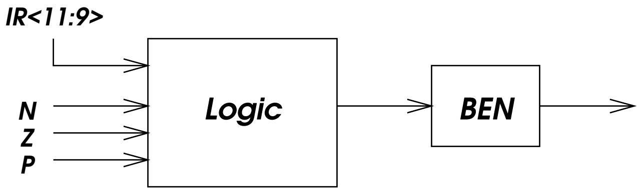

Figure 3: Branch Enable Logic