

12/5/25

①

# Parallel Processing

Wider GPU

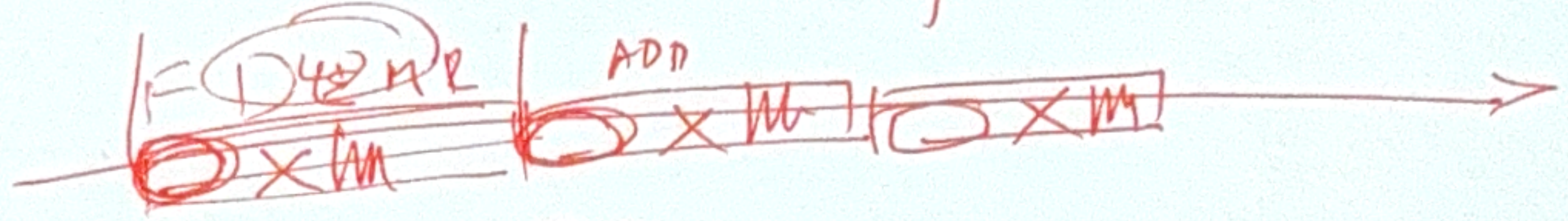


SIMD

32

<sup>25</sup>

2



120

1ms

60

2ms  
2hrs

	1	2	3	4	5	6	7	8
Instruction 1	F	D	M	W	B			
Instruction 2		F	D	M	W	B		
Instruction 3			F	D	M	W	B	
Instruction 4				F	D	M	W	B
Instruction 5					F	D	M	W
Instruction 6						F	D	M
Instruction 7							F	D
Instruction 8								F

BRANCH  
PREDICTOR  
QUICK

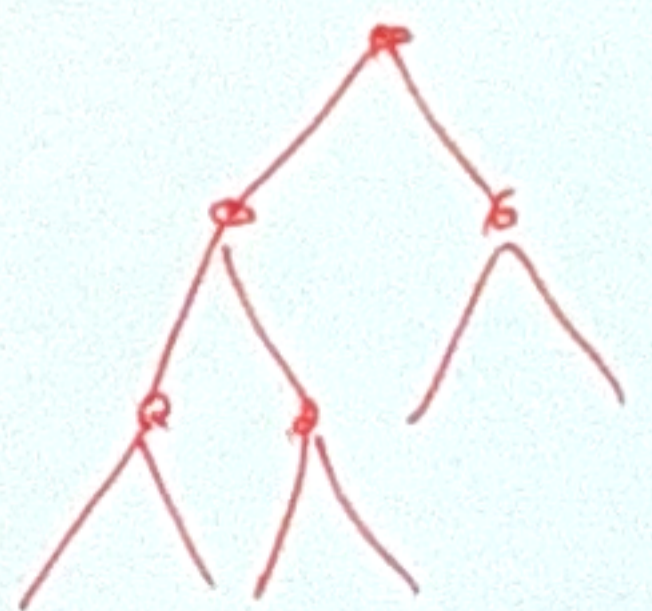
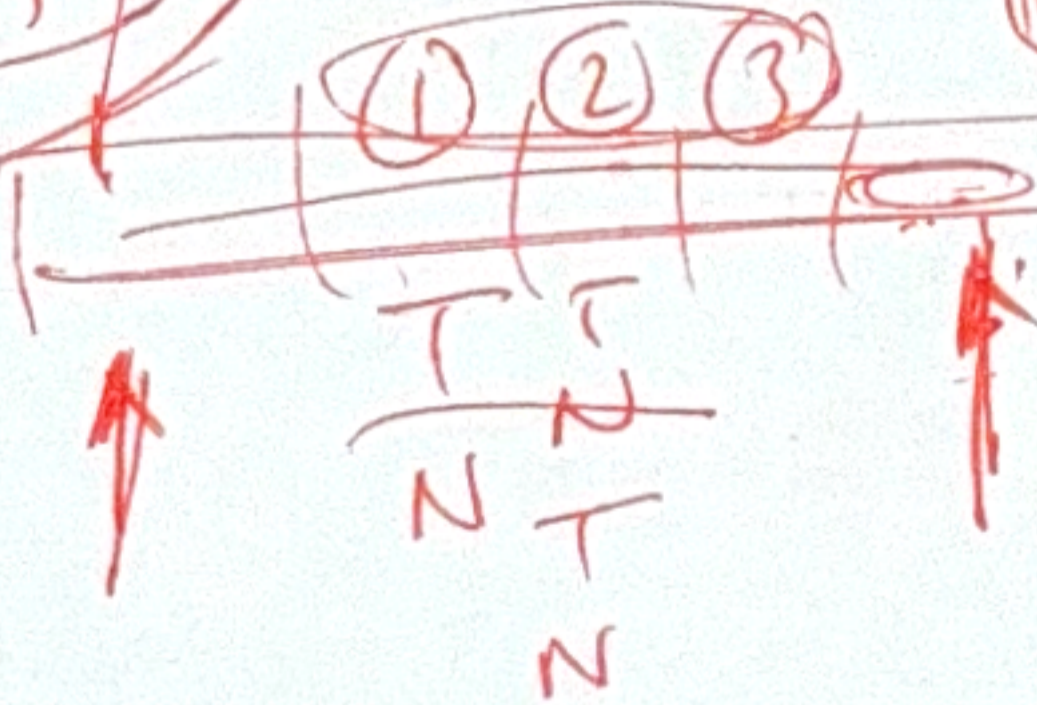
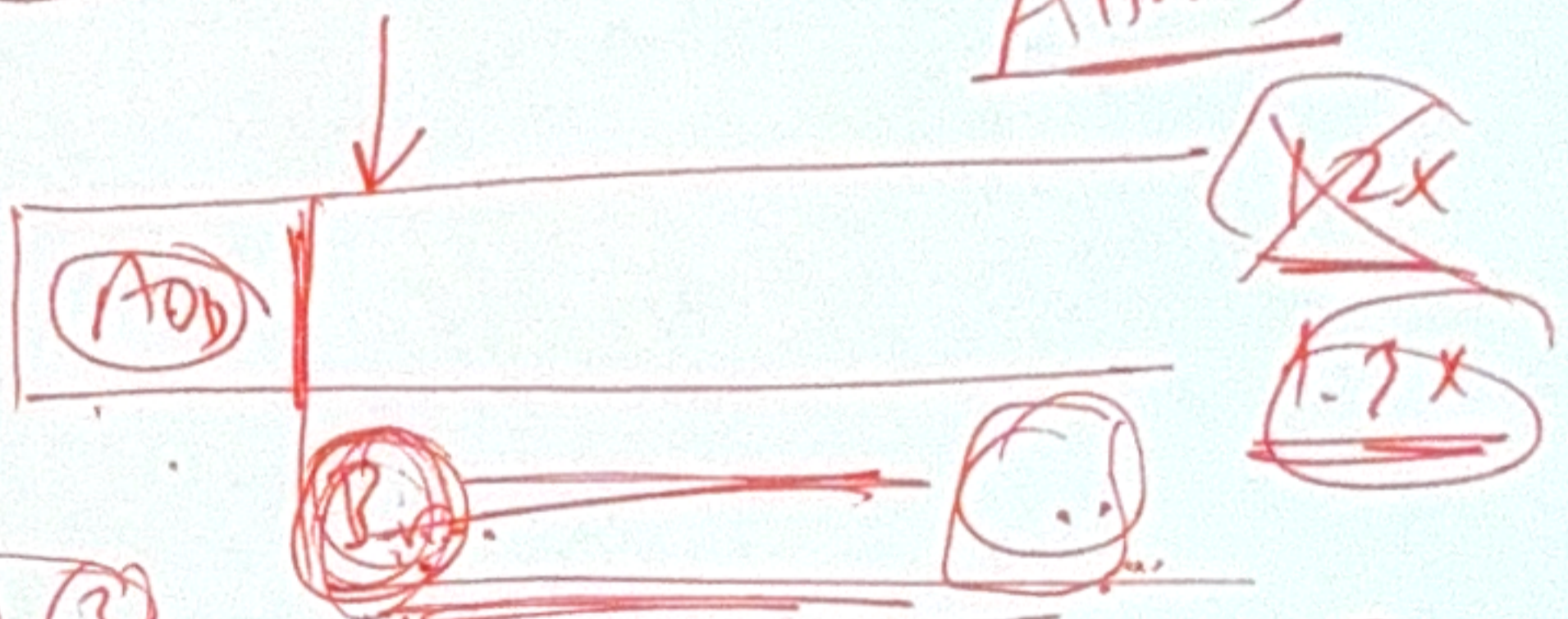
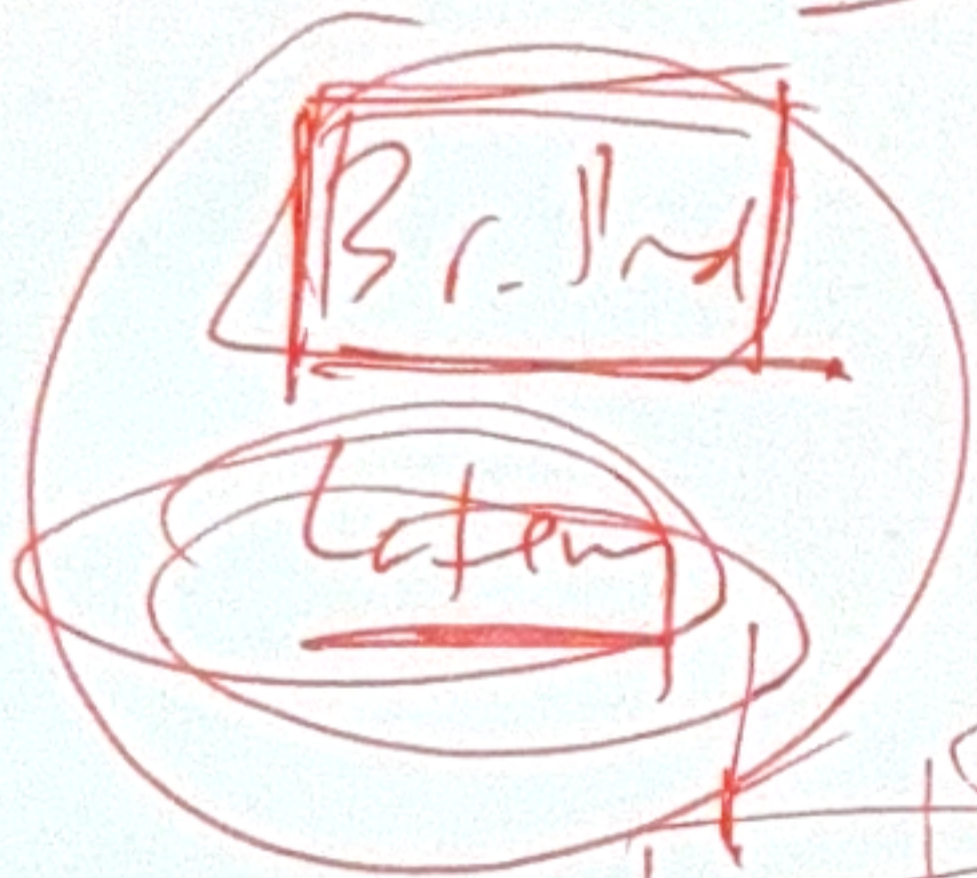
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(2)

802  
 978  
 986  
 212

202  
 32  
 20  
 1.2

1991  
 Andre  
 Seznec



AHMAD

CAI

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(3)

