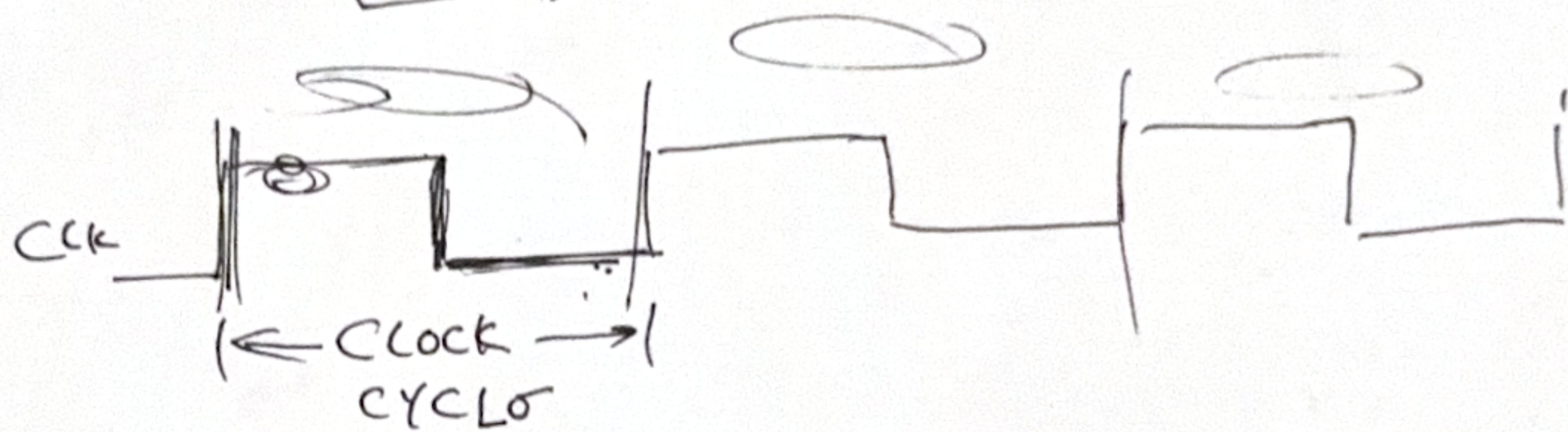
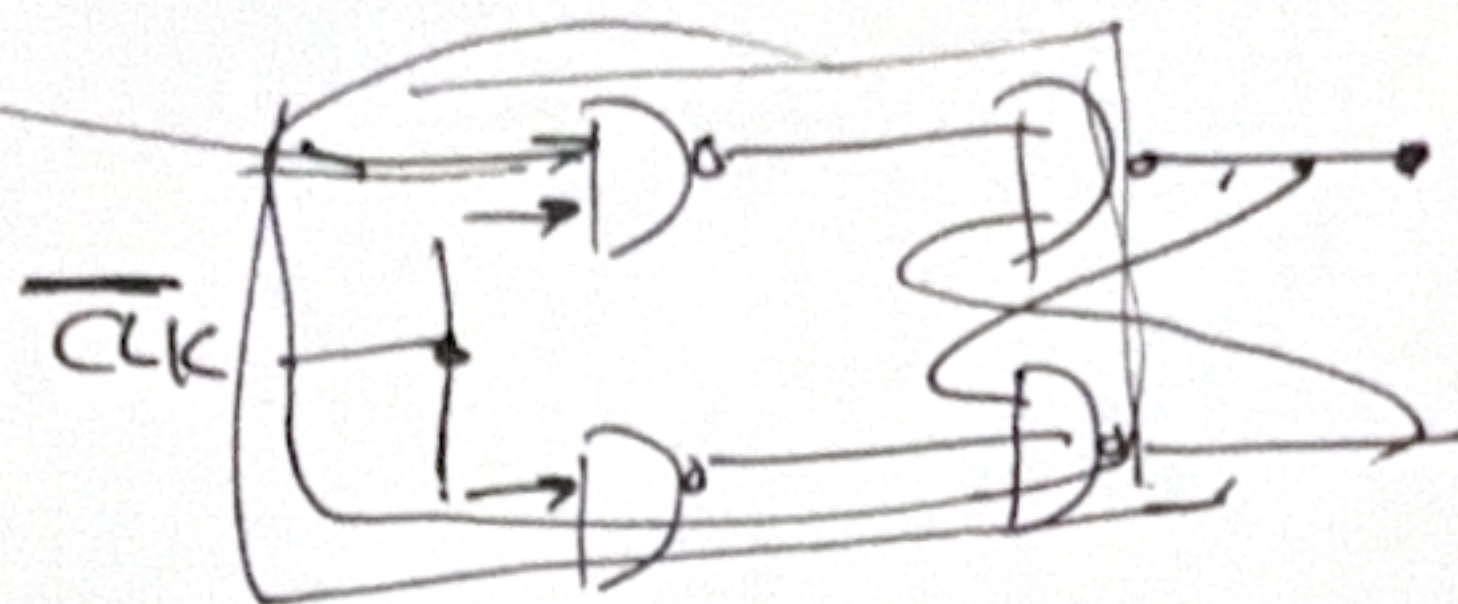
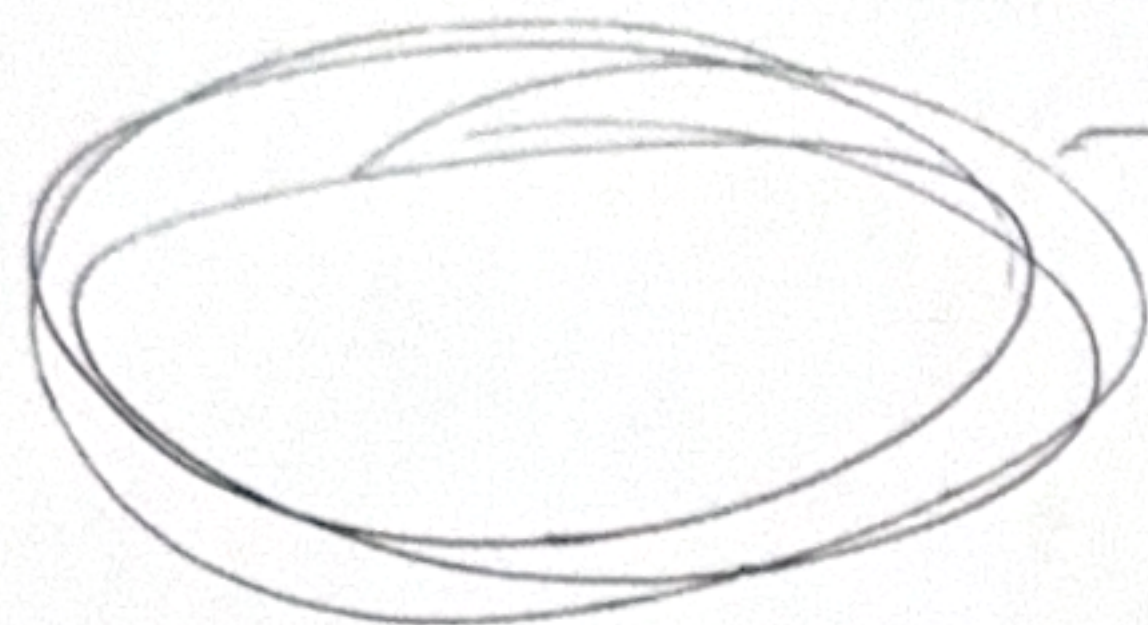


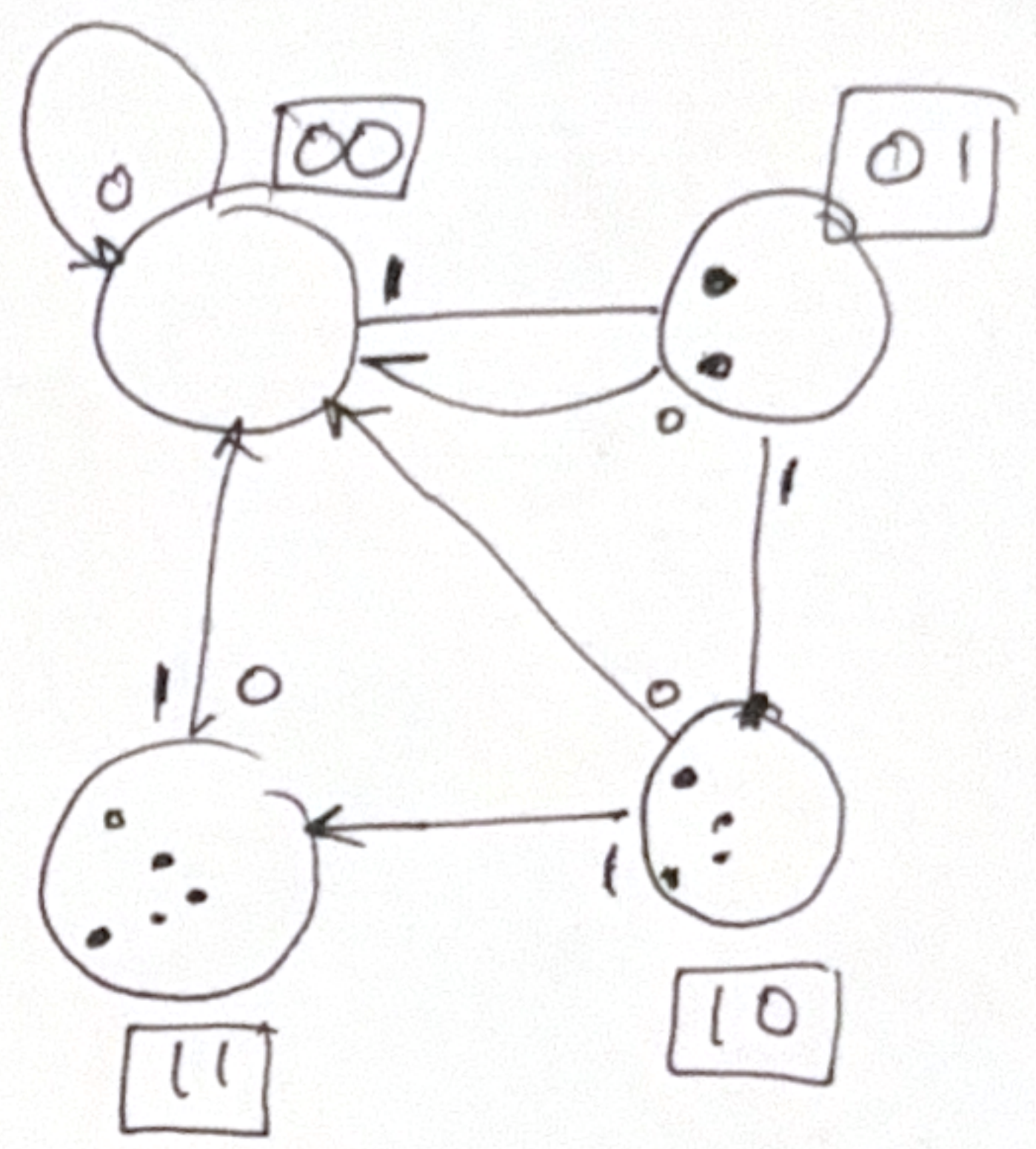
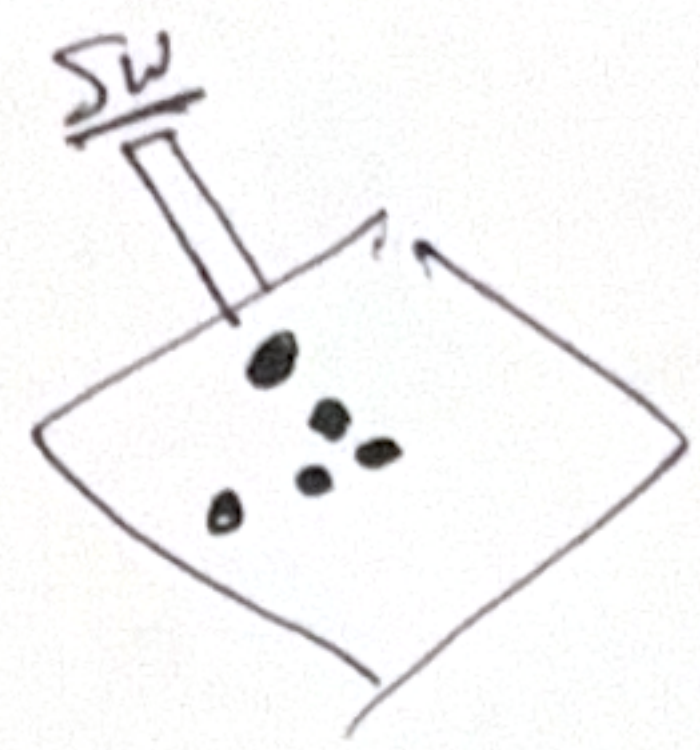
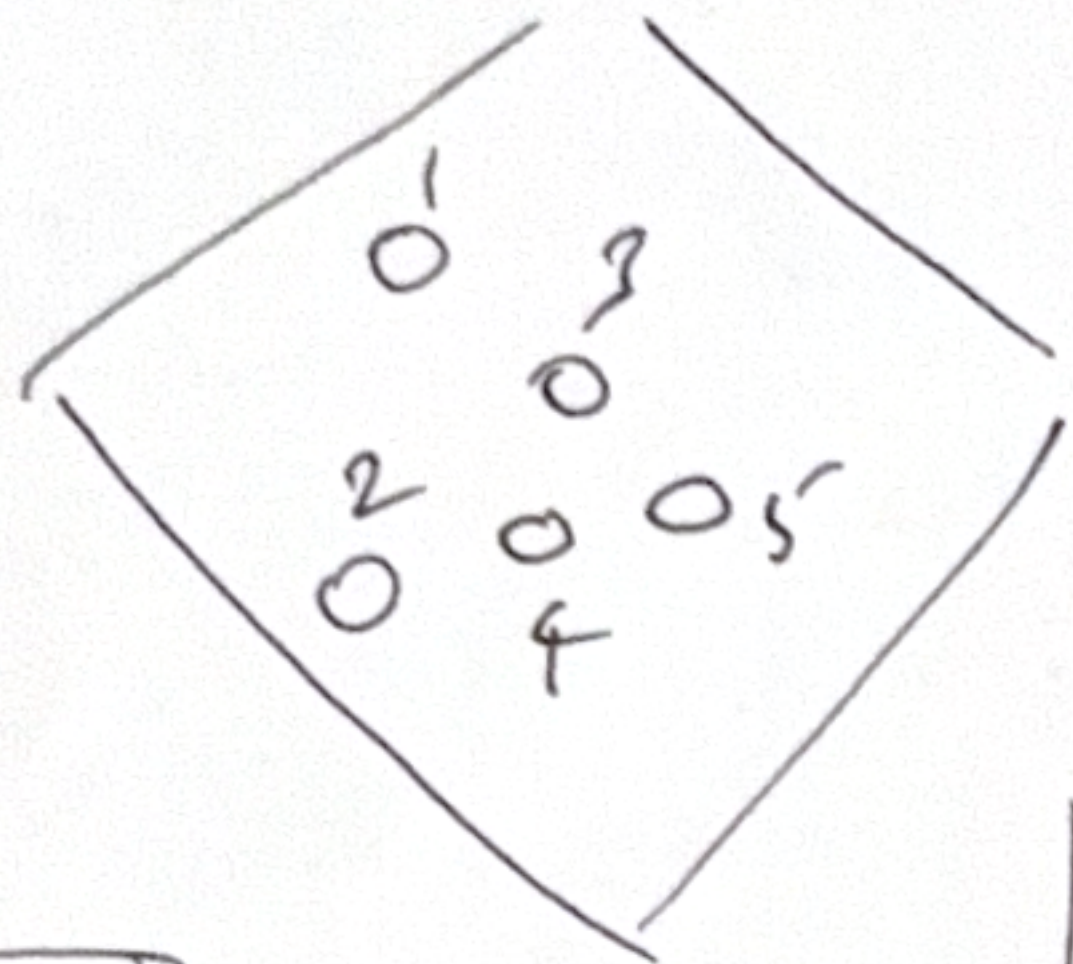
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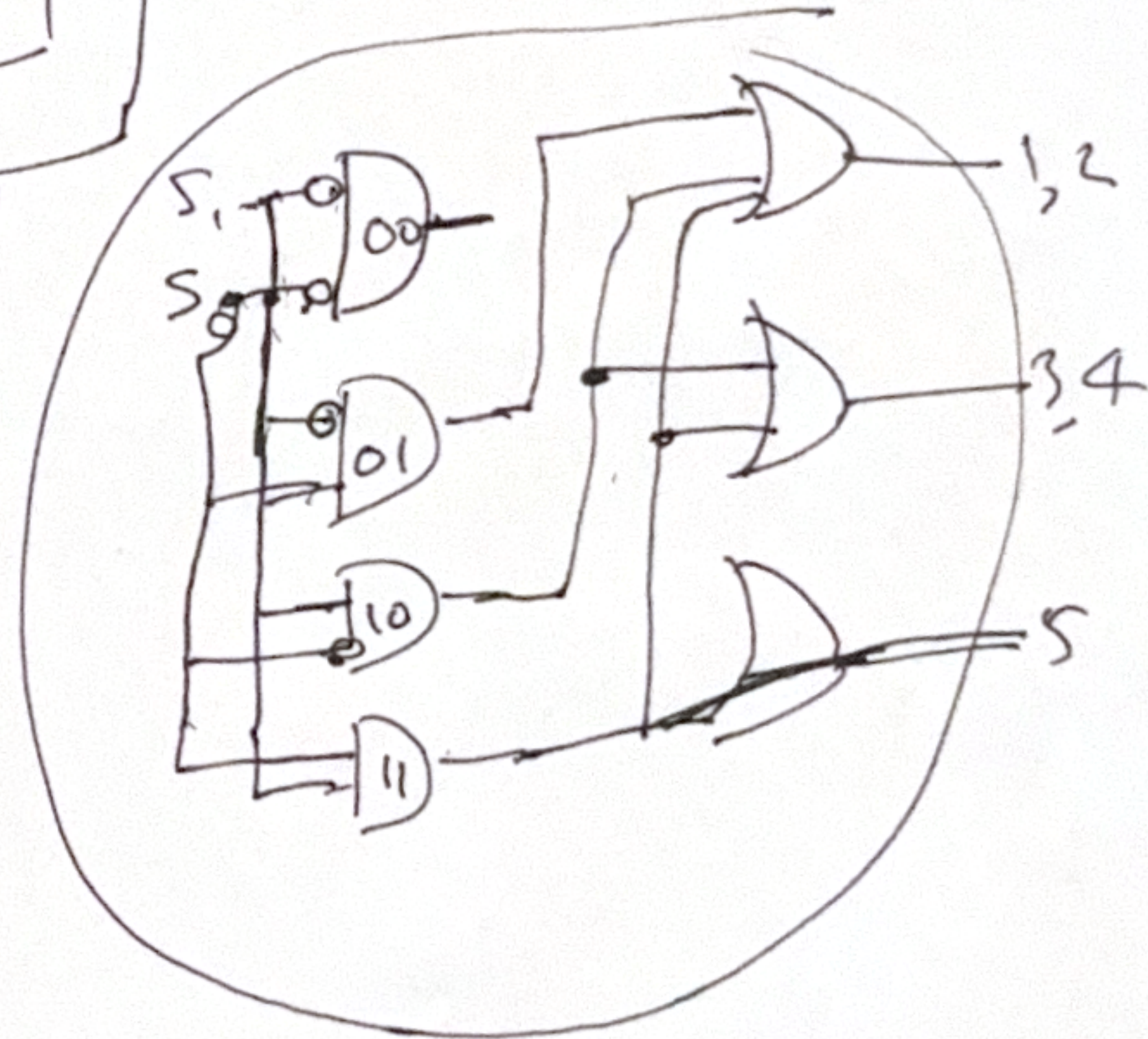
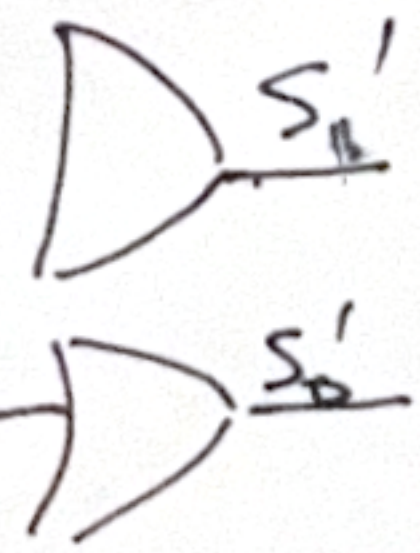
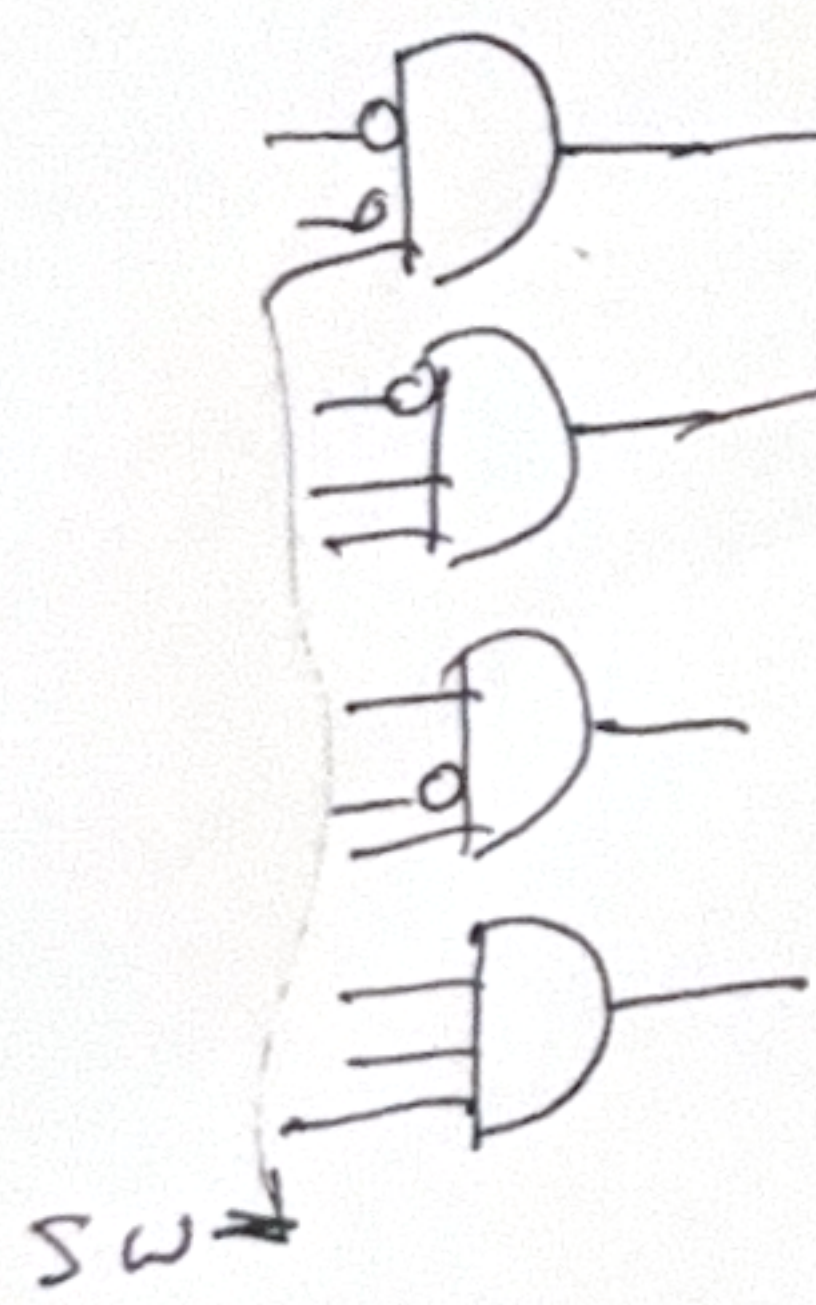
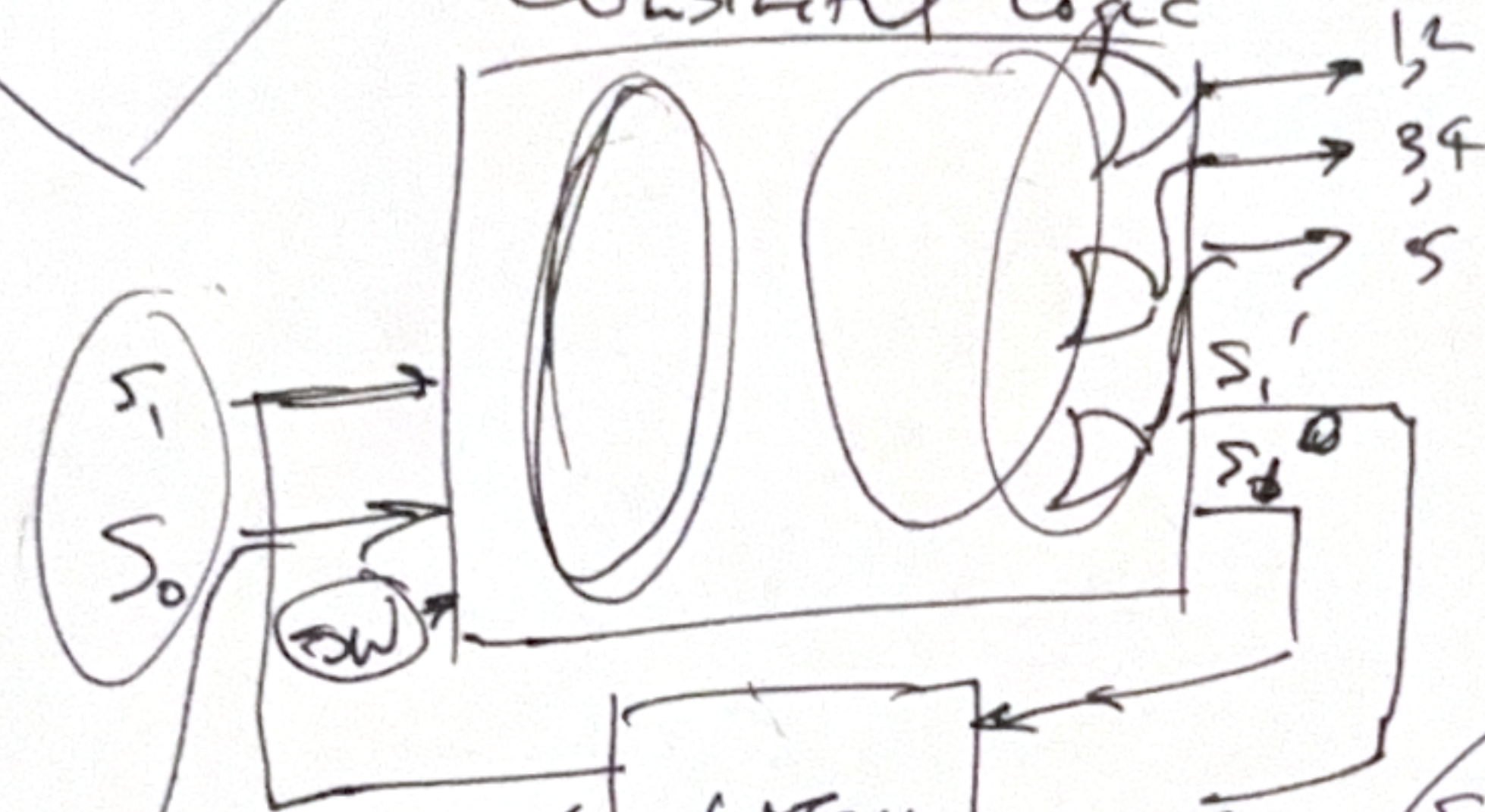
(2)



Moore Machine

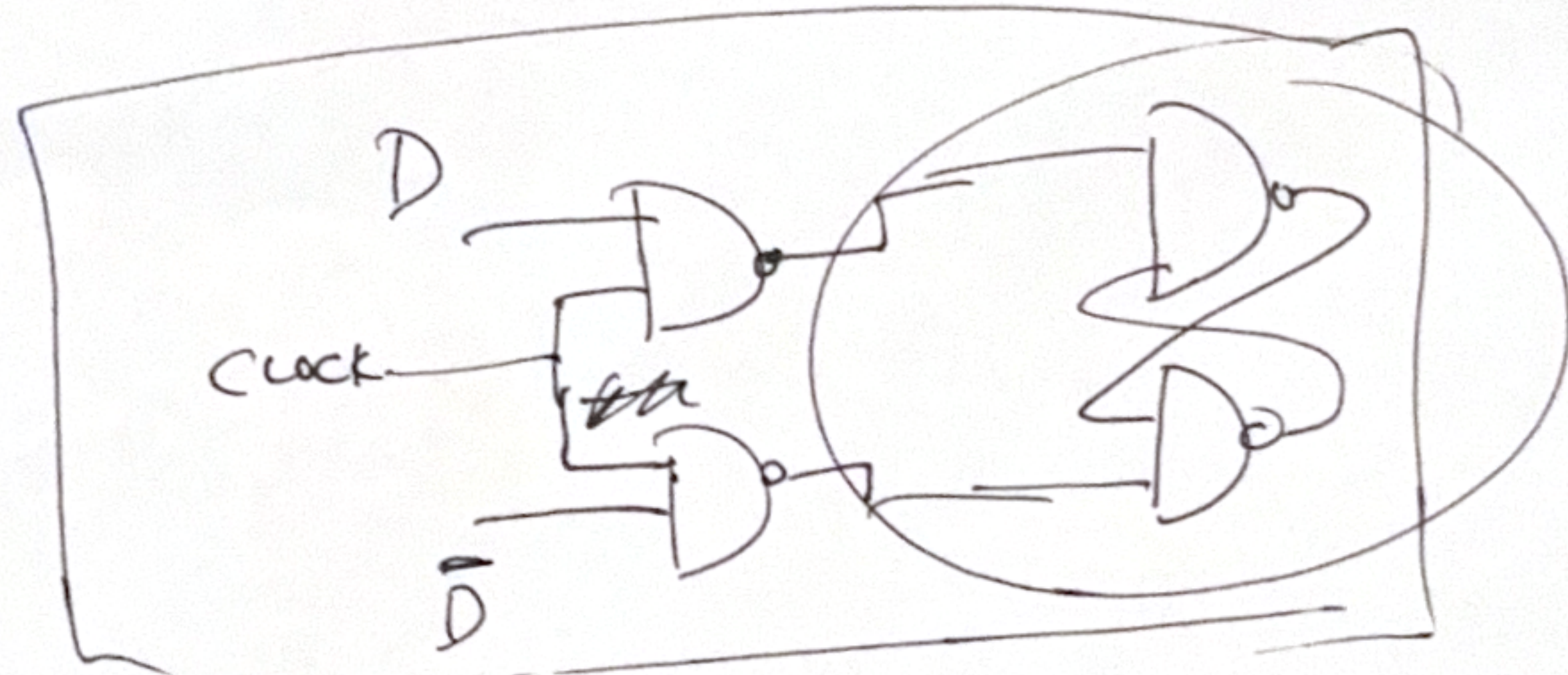
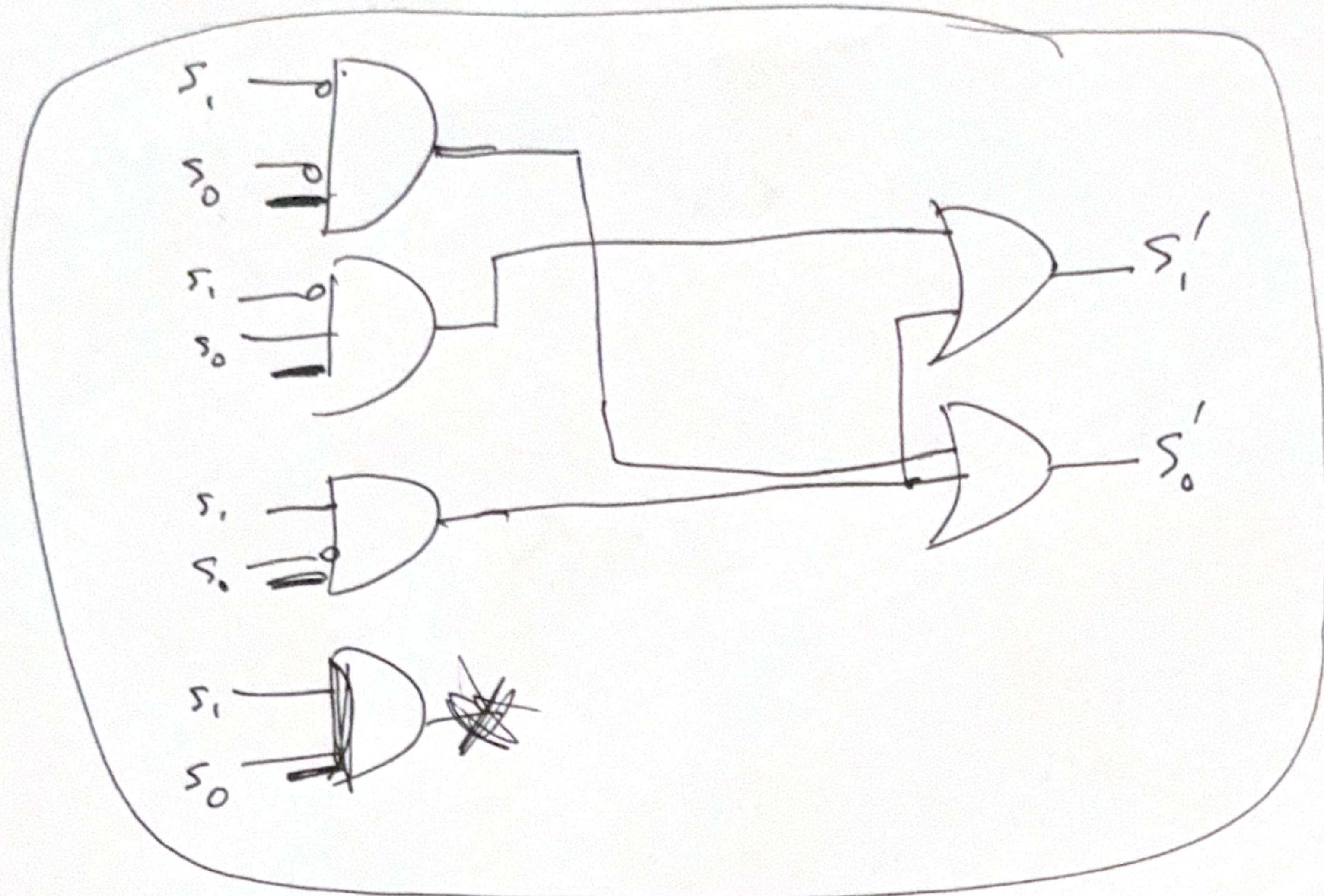
Mealy Machine

Combinational Logic

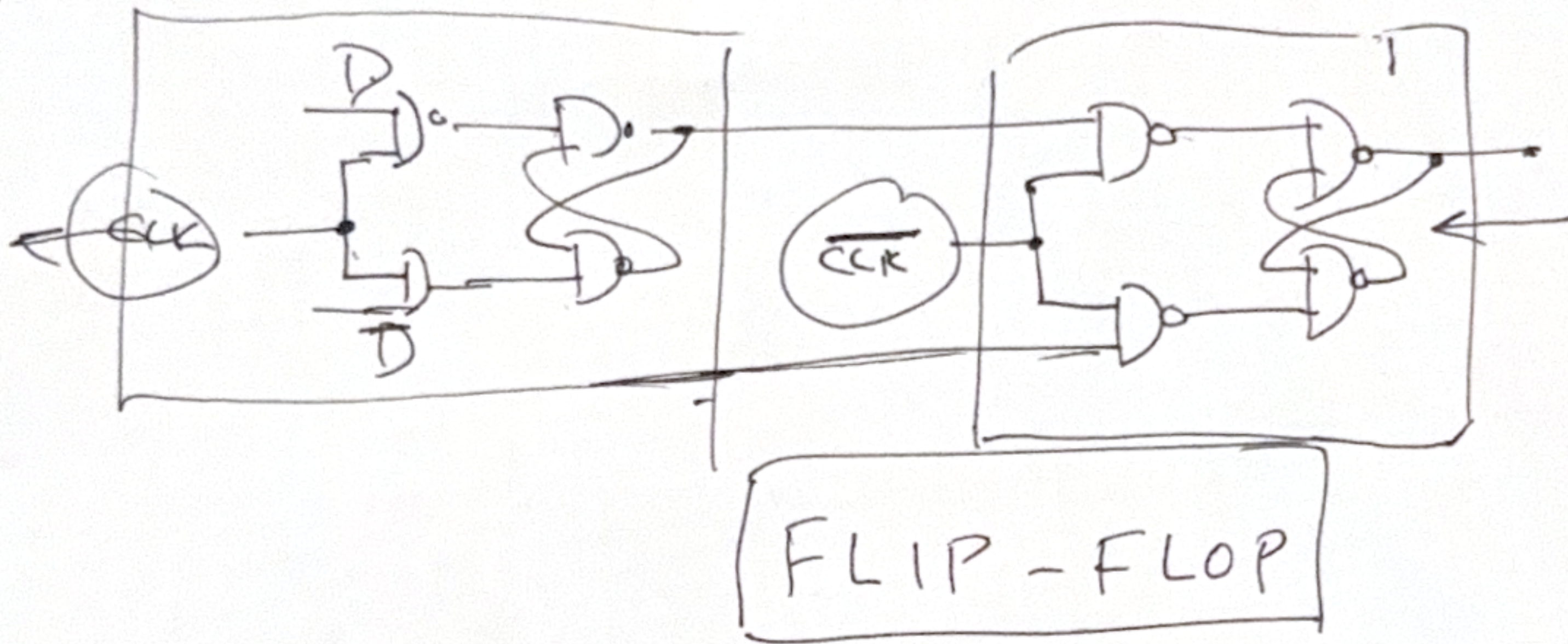


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(4)



1 BIT REGISTER