

Errata as of 5/12/2012

Embedded Systems: Real-Time Operating Systems for the ARM® Cortex™-M3

Page 17, Figure 1.12, change PD9 to PD0 on CAN interface of LM3S8962

Page 29. Change

Figure 1.16. The carry bit is set on addition and subtraction when crossing the 255-0 boundary.

To

Figure 1.16. The carry bit is set on addition when crossing the 255-0 boundary. The carry bit is cleared on subtraction when crossing the 255-0 boundary.

Page 30 Change

If the two inputs to an addition or subtraction operation are considered as unsigned, then the C bit (carry) will be set if the result does not fit. In other words, after an unsigned addition, the C bit is set if the answer is wrong.

To

If the two inputs to an addition operation are considered as unsigned, then the C bit (carry) will be set if the result does not fit. In other words, after an unsigned addition, the C bit is set if the answer is wrong. If the two inputs to a subtraction operation are considered as unsigned, then the C bit (carry) will be clear if the result does not fit.

Page 31 Add line over top of C equation after subtraction

$$C: \text{ unsigned overflow} \quad C = \overline{X_{31} \& M_{31}} \mid M_{31} \& R_{31} \mid R_{31} \& X_{31}$$

Page 113, line 5, change “on the disk” to “in the heap”

Page 116, 5 lines above Figure 3.8, change “Blocks are allocated only as an even number of bytes.” to “The number of bytes in a block is divisible by four.”

Page 120, line 10, change “Chapter 5” to “Chapter 4”

Page 127, 5 lines from bottom of the page, change “Chapter 5” to “Section 3.6”

Page 130, line 4, change “Programs 3.4 and 3.4” to “Programs 3.4 and 3.5”

Page 157, 9 lines from bottom of the page, change “Figure 4.31” to “Figure 4.1”

Page 183, 3rd line, change “scheduled” to “schedule”

Page 183, 5th line from bottom. Change “leave” to “left”

Page 197, first line in second paragraph, change “another” to “other”

Page 293, first line of Section 9.1, change “one or” to “one or two”